

# VITA 57.1 FMC EXTENDER CARD

#### VITA 57.1-COMPLIANT FMC MODULE INCREASES BOARD-TO-BOARD SPACING OVER FPGA CARRIER CARDS

Engineers prototyping with industry-standard FPGA evaluation and development kits often leverage the FMC interface for I/O expansion that fits their application needs. In some cases, the mating height of the standard FMC connectors may prevent fully leveraging the connectivity options of all FMC modules.

In response to that need, Samtec has developed the FMC Extender Card for placement between FPGA Carrier Cards and FMC Modules. This increased space can be used for additional I/O expansion during development.

The FMC Extender Card also provides a cost-effective option for extending the life of the FPGA Carrier Card FMC connectors used as test platforms.

#### **FEATURES:**

- High Pin Count (HPC) VITA 57.1 FMC male connector (Samtec P/N: ASP-134488-01)
- High Pin Count (HPC) VITA 57.1 FMC female connector (Samtec P/N: ASP-134486-01)

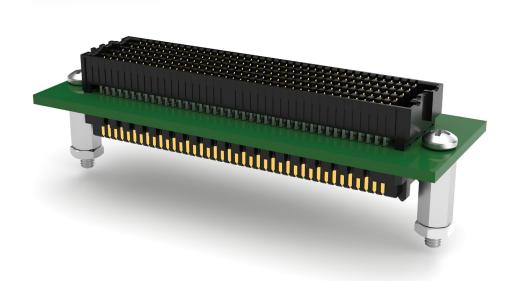
#### **APPLICATIONS:**

- FPGA development
- FPGA carrier card development
- FPGA carrier cards used in test platform
- High-speed ADCs and DACs
- Next generation RF connectivity

#### KIT INCLUDES:

- VITA 57.1 FMC Extender Card
- Quick Start Card

- Provides direct pass-through connectivity for all 400 pins from the HPC male to HPC female connectors
- Features optimized SI performance via Samtec Final Inch® BOR PCB trace routing for HPC connectors



Samtec Part No. REF-228680-01 Pictured

## **VITA 57.1 FMC EXTENDER CARD**

For additional information or support on your next generation FMC design, please visit www.samtec.com/fmc-extender or contact KitsAndBoards@Samtec.com.

- VITA 57.1 FMC form factor
- VITA 57.1 HPC connectors (male and female)
- FMC I/O voltage: VADJ=1.2V, 1.5V, 2.5V or 3.3V (FPGA carrier card dependent)

### VITA 57.1 FMC EXTENDER CARD BLOCK DIAGRAM

ASP-134488-01		ASP-134486-01
K[1:40]	← →	K[1:40]
J[1:40]	←	J[1:40]
H[1:40]	← →	H[1:40]
G[1:40]	←>	G[1:40]
F[1:40]	←>	F[1:40]
E[1:40]	←>	E[1:40]
D[1:40]	← →	D[1:40]
C[1:40]	←	C[1:40]
B[1:40]	← →	B[1:40]
A[1:40]	<b>←</b> →	A[1:40]

Note: Block diagram shows pin numbers and not signal names



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