

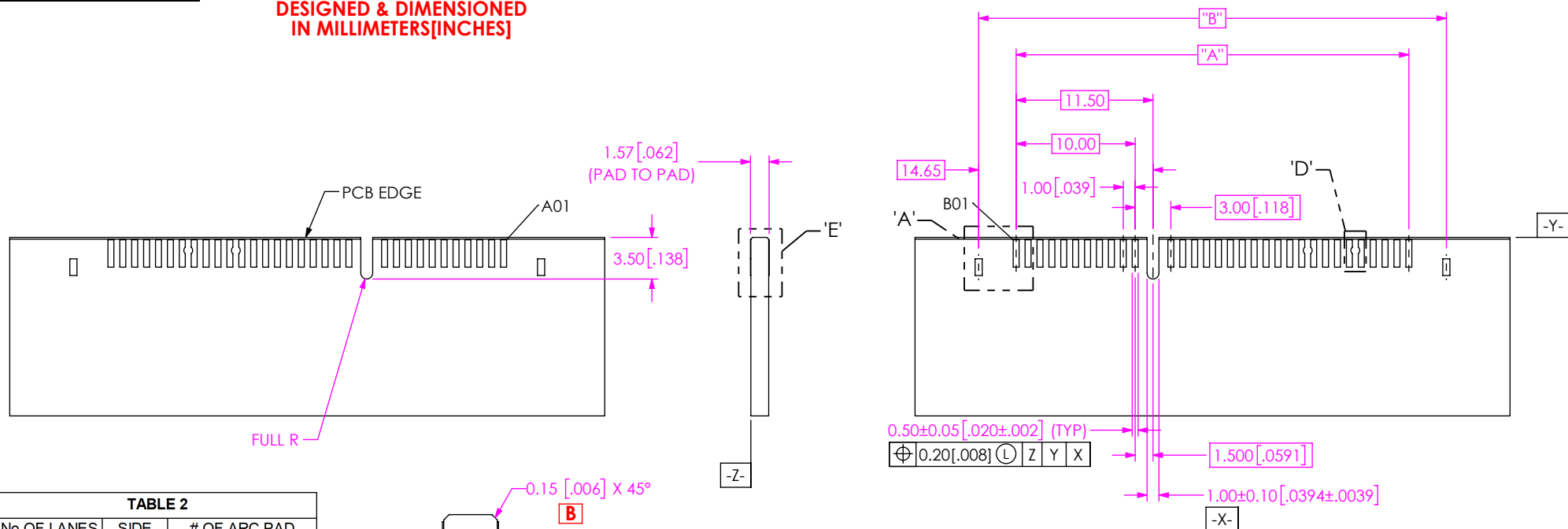
DESIGNED & DIMENSIONED
IN MILLIMETERS[INCHES]

TABLE 2			
No OF LANES	SIDE	# OF ARC PAD	
-04	A	21	22
	B	23	24
	A	25	26
	B	27	28
-08	A	35	36
	B	37	38
	A	39	40
	B	41	42
	A	43	44
	B	45	46
-16	A	52	53
	B	54	55
	A	56	57
	B	58	59
	A	60	61
	B	62	63
	A	64	65
	B	66	67
	A	68	69
	B	70	71
	A	72	73
	B	74	75
	A	76	77
	B	78	79

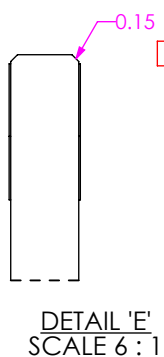
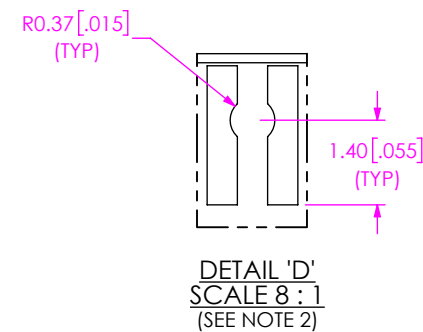
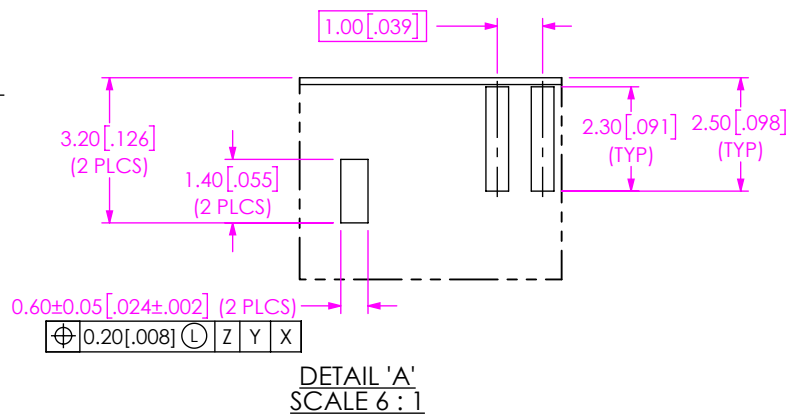
FIG 1
PCIE-G5-04-01-X-EM-WT-XR FOOTPRINT SHOWN

TABLE 1		
No OF LANES	"A"	"B"
-01	19.00 [.748]	25.30 [.996]
-04	33.00 [1.299]	39.30 [1.547]
-08	50.00 [1.969]	56.30 [2.217]
-16	83.00 [3.268]	89.30 [3.516]

NOTES:

- ALL DIMENSIONS ARE SYMMETRIC ABOUT THE CENTERLINE.
- OPTIONAL PAD ARC FOR SIGNAL PADS ONLY. (SEE TABLE 2 FOR ARC PAD #).
- ALLOW GROUND VIA BETWEEN SIGNAL PADS.

B

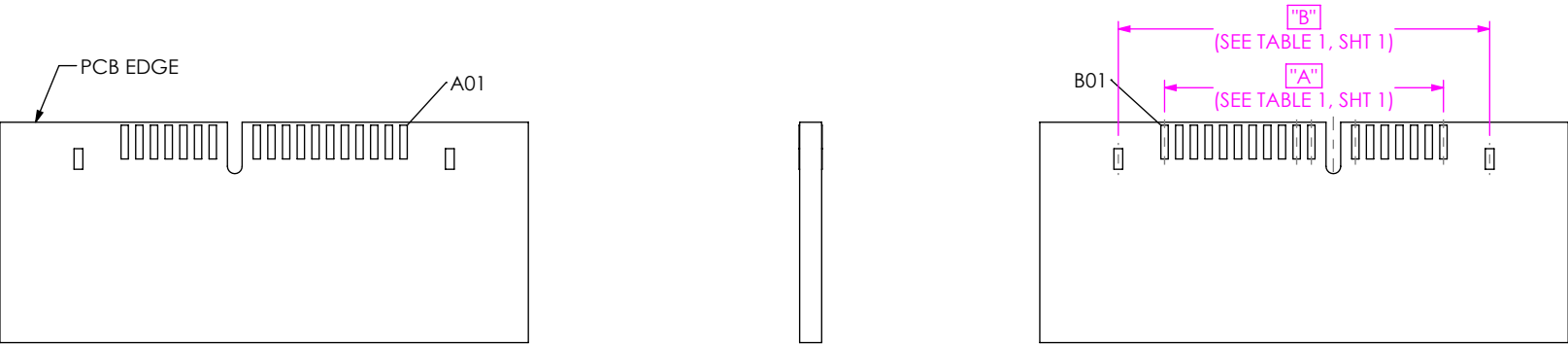


FIG 4
PCIE-G5-01-01-X-EM-WT-XR FOOTPRINT SHOWN
(DIFFERENT AS SHOWN, OTHERWISE SAME AS FIG 1)

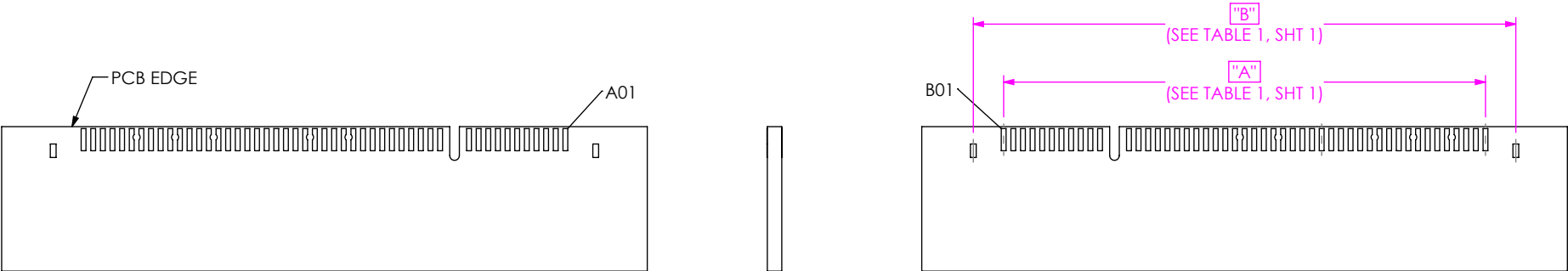


FIG 5
PCIE-G5-08-01-X-EM-WT-XR FOOTPRINT SHOWN
(DIFFERENT AS SHOWN, OTHERWISE SAME AS FIG 1)

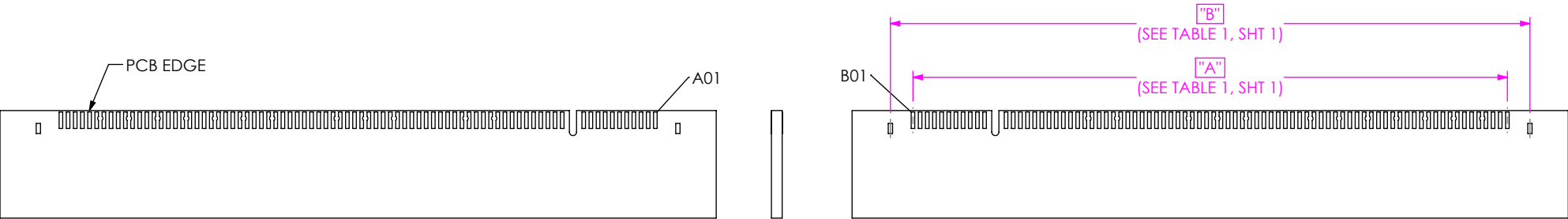
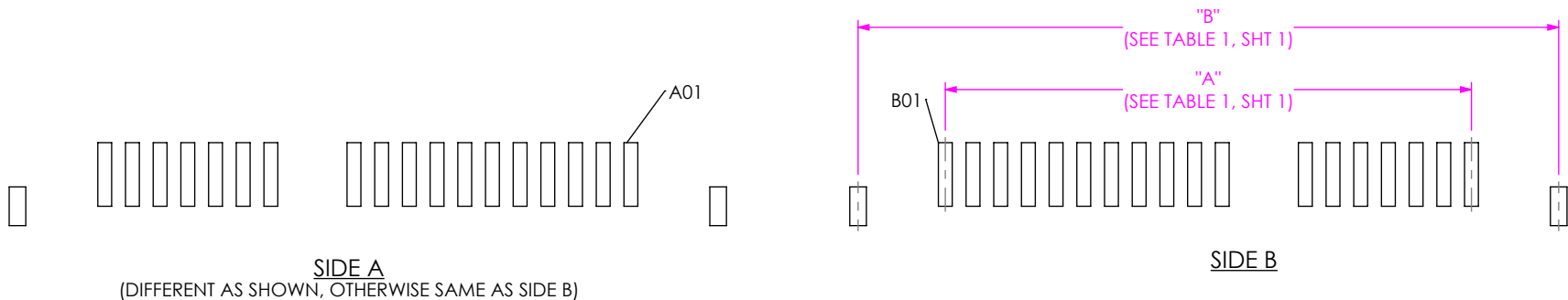
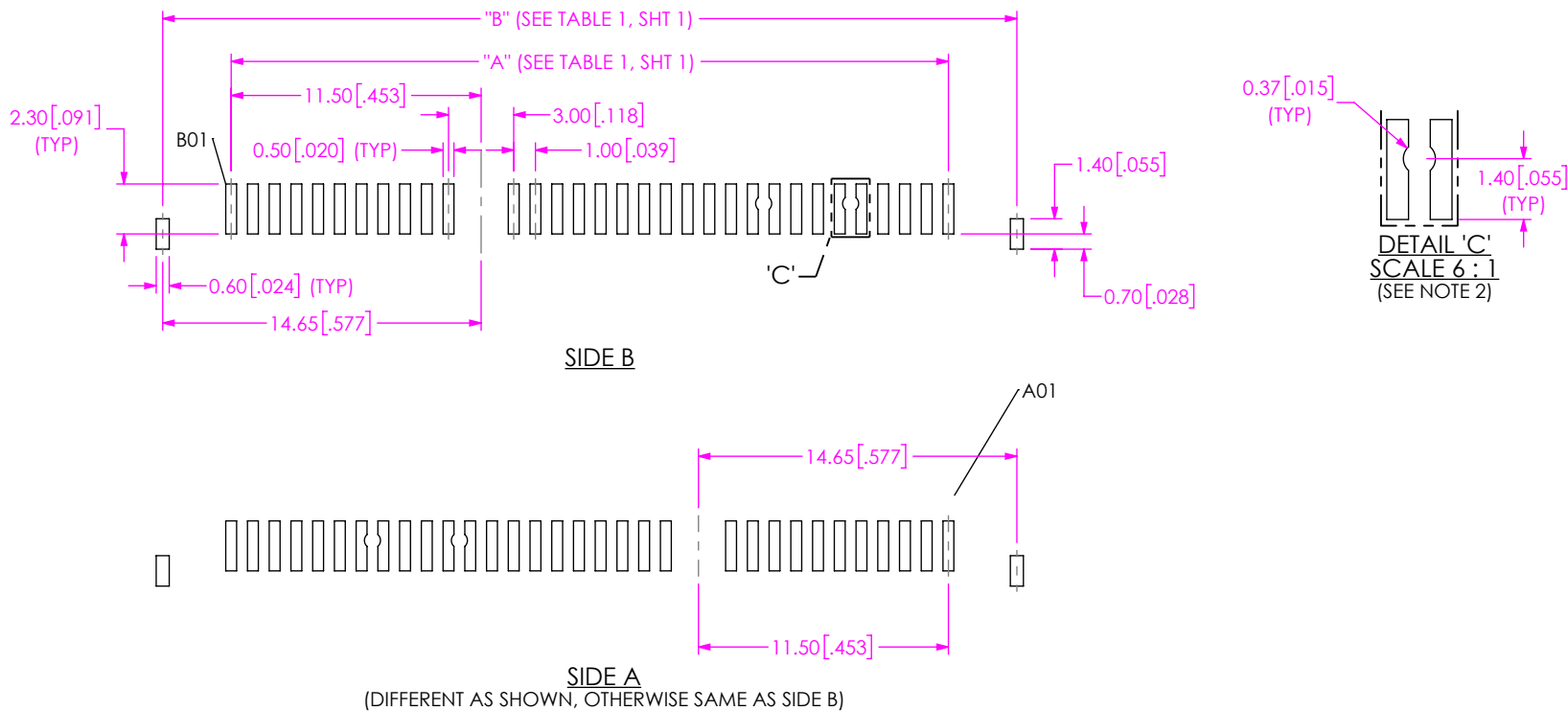


FIG 6
PCIE-G5-16-01-X-EM-WT-XR FOOTPRINT SHOWN
(DIFFERENT AS SHOWN, OTHERWISE SAME AS FIG 1)

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NOTE:
STENCIL TO BE .0060[.152] THICK.

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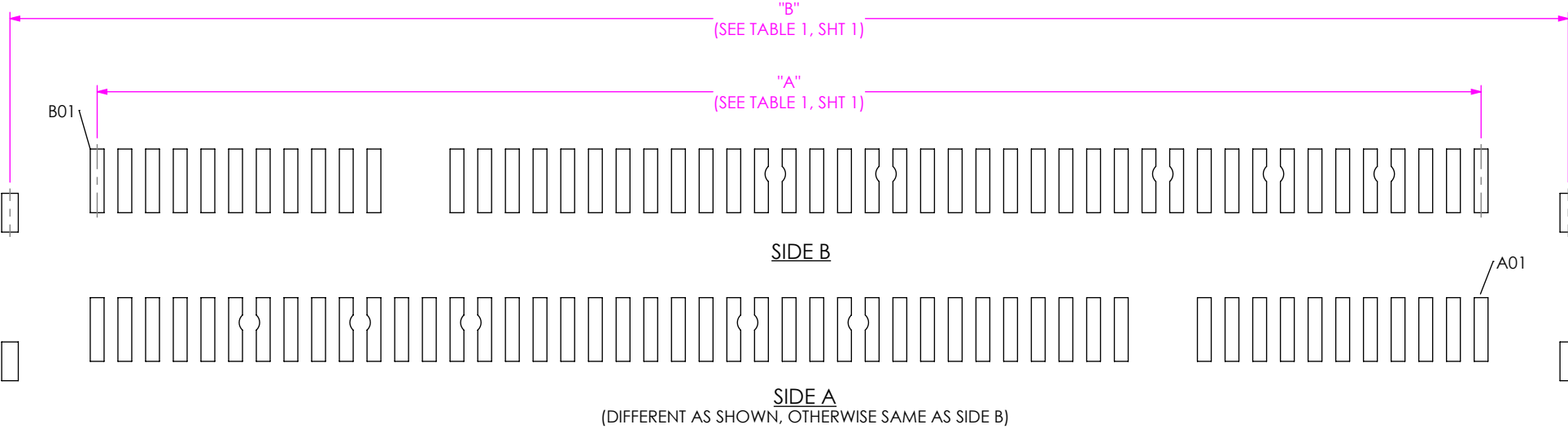


FIG 8
PCIE-G5-08-01-X-EM-WT-XR STENCIL SHOWN
(DIFFERENT AS SHOWN, OTHERWISE SAME AS FIG 2)

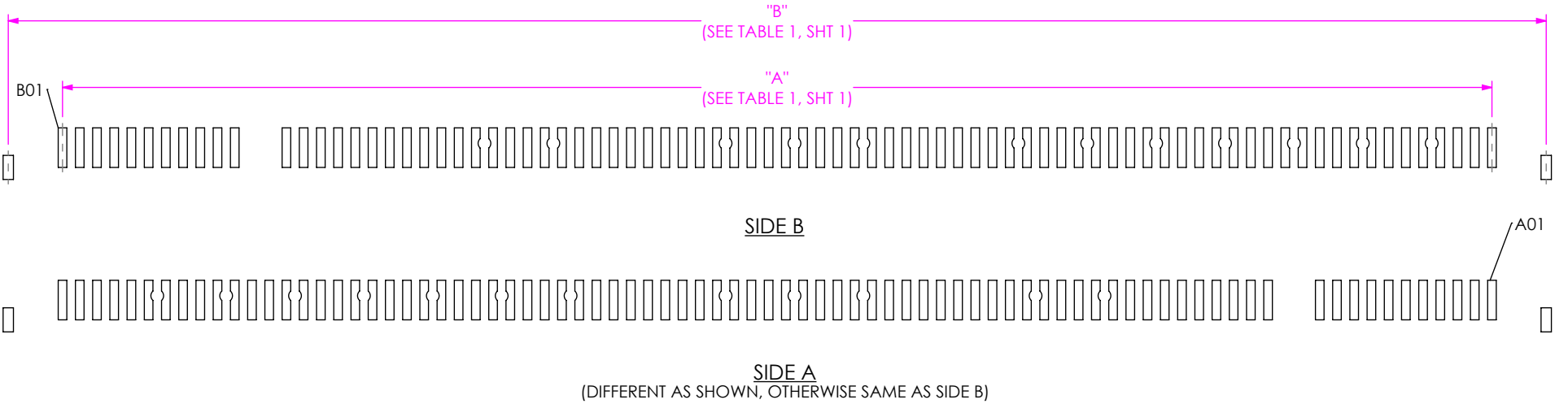


FIG 9
PCIE-G5-16-01-X-EM-WT-XR STENCIL SHOWN
(DIFFERENT AS SHOWN, OTHERWISE SAME AS FIG 2)

NOTE:
STENCIL TO BE .0060[.152] THICK.

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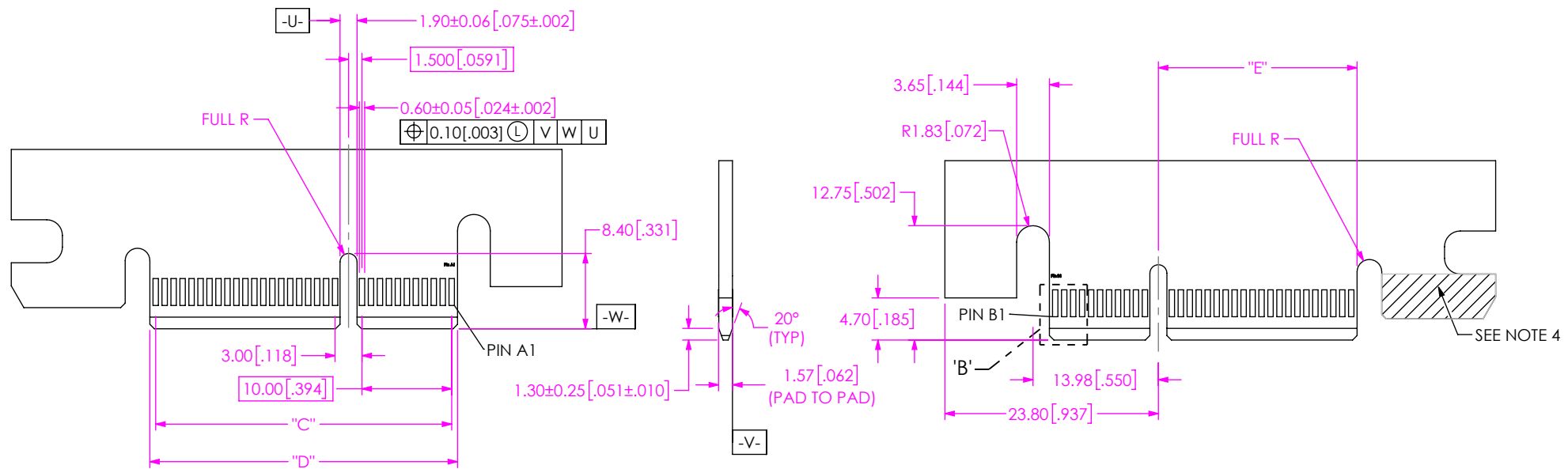


FIG 3
PCIE-G5-04-01-X-EM-WT-XR MATING CARD SHOWN

TABLE 3			
No OF LANES	"C"	"D"	"E"
-01	19.00 [0.748]	20.30 [0.799]	8.15 [0.321]
-04	33.00 [1.299]	34.30 [1.350]	22.15 [0.872]
-08	50.00 [1.969]	51.30 [2.020]	39.15 [1.541]
-16	83.00 [3.268]	84.30 [3.319]	72.15 [2.841]

NOTES:

1. PLATING TO BE .000030 INCHES GOLD WITH A HARDNESS OF 130-200 KNOOP PER ASTM B 488 TYPE 2, GRADE C.
2. SEE PCIE-G5-XX-XX-X-EM-WT-ASM FOR SIGNAL AND GROUND PINOUT.
3. FOR PROPER CONTACT REGISTRATION, PLATING BUS TIES TO BE THE WIDTH OF THE EDGE FINGER.
4. NO COPPER OR SOLDER MASK IS ALLOWED IN CROSS HATCHED ARDA, BOTH SIDES.

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