REVISION A

10/30/2017	ECN-312584

PCIE-LP MATED VIEWS

DESIGNED & DIMENSIONED IN MILLIMETERS[INCHES]

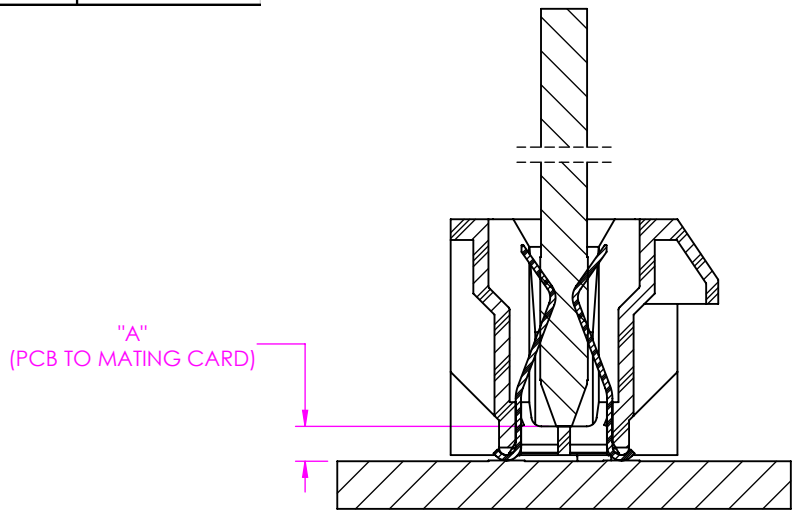

FIG 1
PCIE-LP

TABLE 1				
	"A" FULLY MATED	"A" MAX ALLOWABLE	CONTACT WIPE	
			MAX	MIN
PCIE-LP	1.15 [0.045]	3.50 [0.138]	2.73 [0.107]	0.38 [0.015]

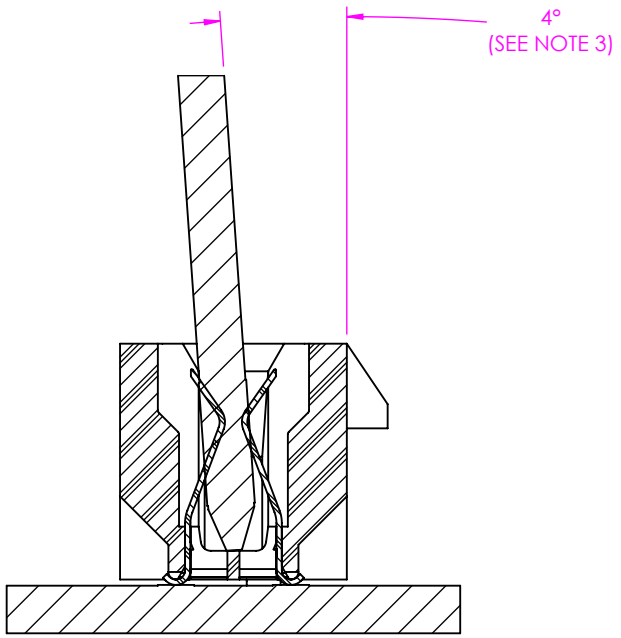

FIG 2
PCIE-LP

NOTES:
1. ALL DIMENSIONS BASED ON NOMINAL VALUES FOR SAMTEC RECOMMENDED PCB LAYOUTS.
2. ALTHOUGH MAXIMUM DIMENSIONS OF ALLOWABLE SEPARATION FROM FULLY MATING THE COMPONENTS ARE LISTED, SAMTEC RECOMMENDS FULL MATING.
3. MAXIMUM ALLOWABLE ANGLE AT FULLY MATED NOMINAL CONDITIONS. F:\dwg\sw\exisprod\mated docs\PCIE-LP Mated Document.SLDDRW

520 PARK EAST BLVD, NEW ALBANY, IN 47150
PHONE: 812-944-6733 FAX: 812-948-5047
e-Mail info@SAMTEC.com code 55322