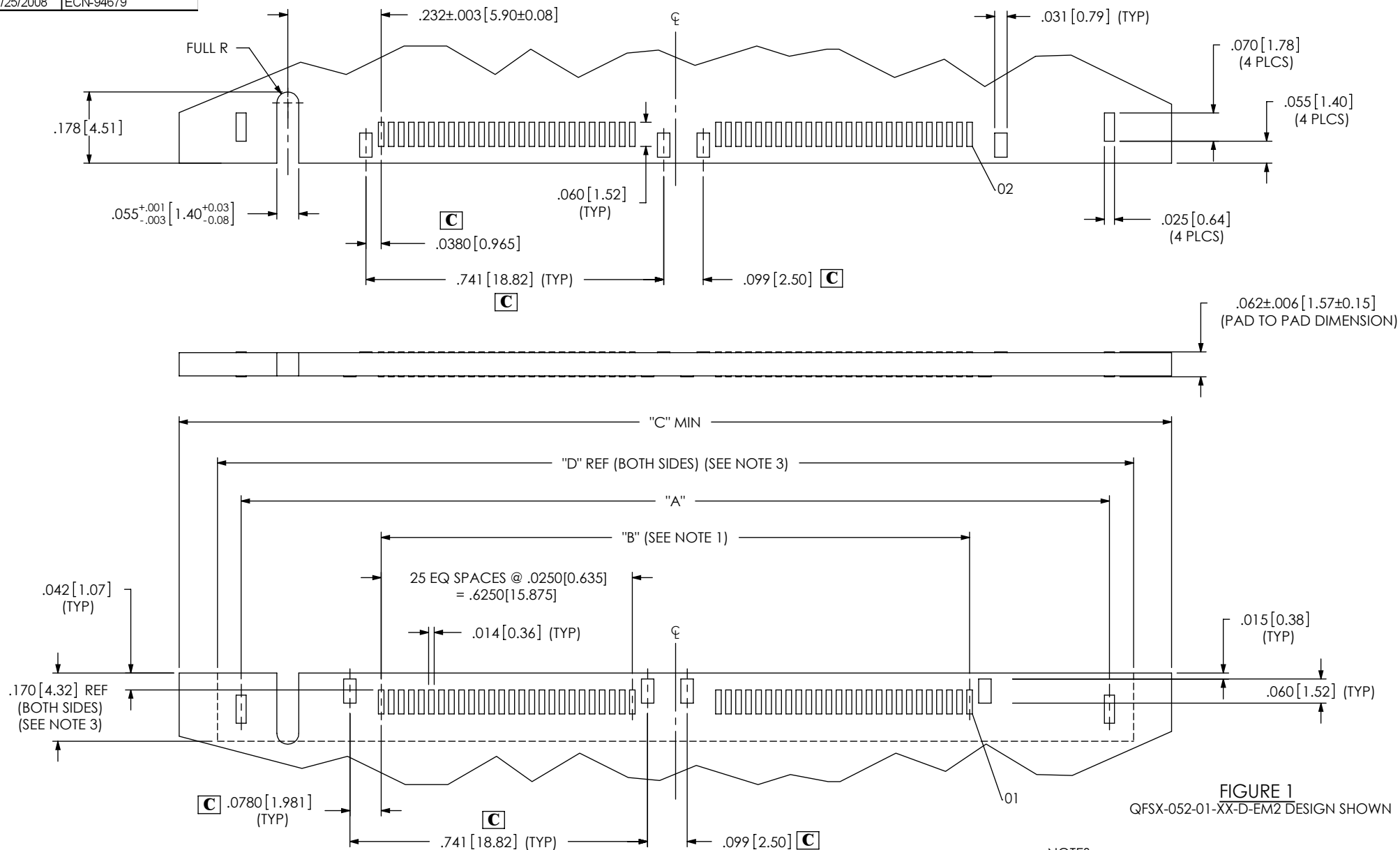


## RECOMMENDED PCB LAYOUT FOR QFSX-XXX-XX-XX-D-EM2-XXX-XX

7/25/2008	ECN-94679
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NOTES:

1. DIMENSION NOT AVAILABLE ON -026 POSITIONS.
2. DESIGNED IN ENGLISH.
3. CONNECTOR ENVELOPE DIMENSIONS; OTHER COMPONENTS PLACED ON PCB SHALL AVOID THIS ZONE.

POSITION	"A"	"B"	"C"	"D"
-026	1.320 [33.53]	---	1.440 [36.58]	1.440 [36.58]
-052	2.160 [54.86]	1.465 [37.21]	2.280 [57.91]	2.280 [57.91]
-078	3.000 [76.20]	2.305 [58.55]	3.120 [79.25]	3.120 [79.25]
-104	3.840 [97.54]	3.145 [79.88]	3.960 [100.58]	3.960 [100.58]
*-130	4.680 [118.87]	3.985 [101.22]	4.800 [121.92]	4.800 [121.92]

\* = NOT TOOLED

F:\DWG\MISC\Mkta\Footprint\QFSX-XXX-XX-XX-D-EM2-XXX-XX-FOOTPRINT.SLDDRW

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**samtec**

520 PARK EAST BLVD, NEW ALBANY, IN 47150  
PHONE: 812-944-6733 FAX: 812-948-5047  
e-Mail: [info@SAMTEC.com](mailto:info@SAMTEC.com) code: 55322

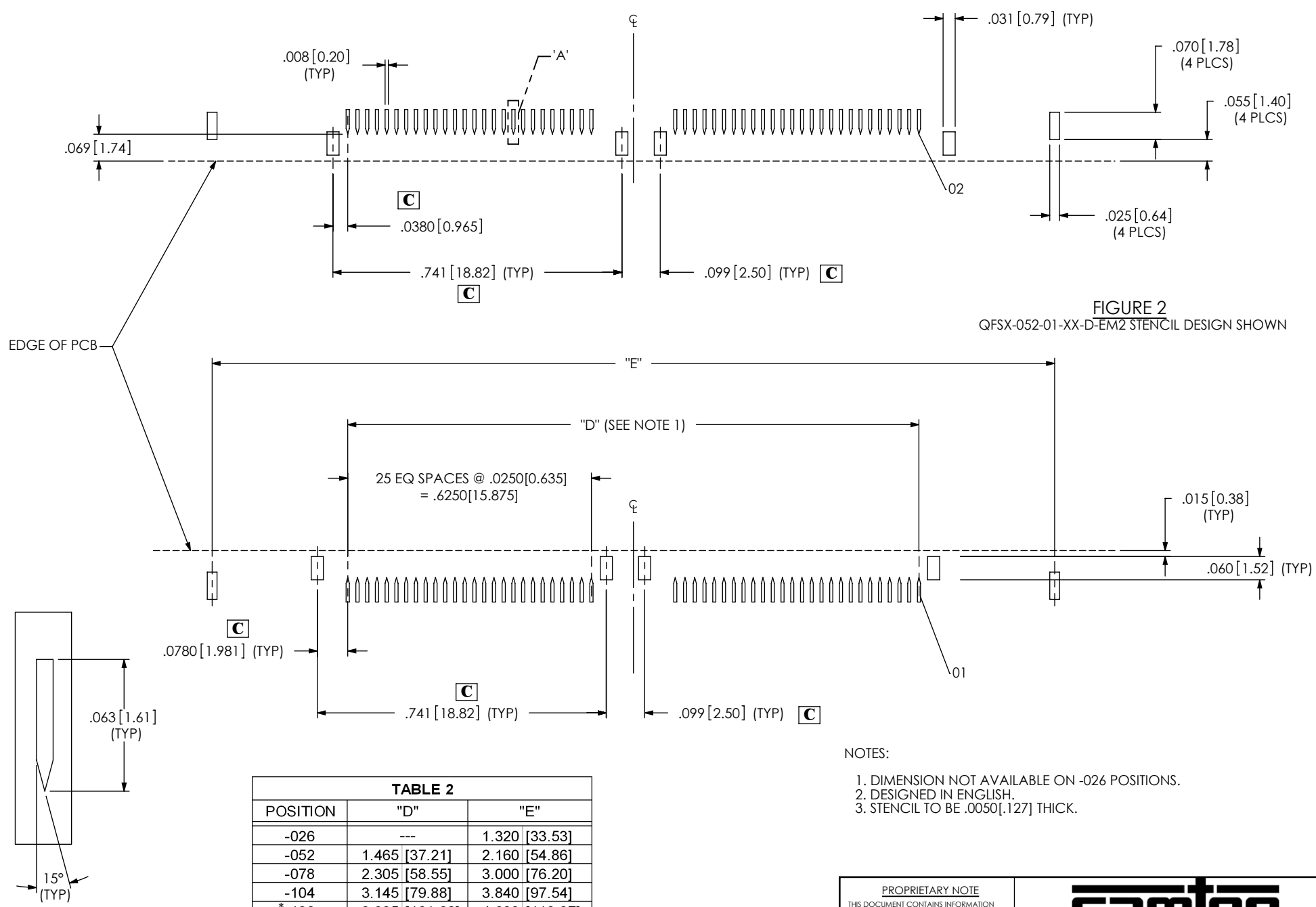


FIGURE 2  
QFSX-052-01-XX-D-EM2 STENCIL DESIGN SHOWN

TABLE 2		
POSITION	"D"	"E"
-026	---	1.320 [33.53]
-052	1.465 [37.21]	2.160 [54.86]
-078	2.305 [58.55]	3.000 [76.20]
-104	3.145 [79.88]	3.840 [97.54]
*-130	3.985 [101.22]	4.680 [118.87]

\* = NOT TOOLED

- NOTES:
1. DIMENSION NOT AVAILABLE ON -026 POSITIONS.
  2. DESIGNED IN ENGLISH.
  3. STENCIL TO BE .0050[.127] THICK.

DETAIL 'A'  
SCALE 16:1

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PHONE: 812-944-6733 FAX: 812-948-5047  
e-Mail: info@SAMTEC.com code: 55322