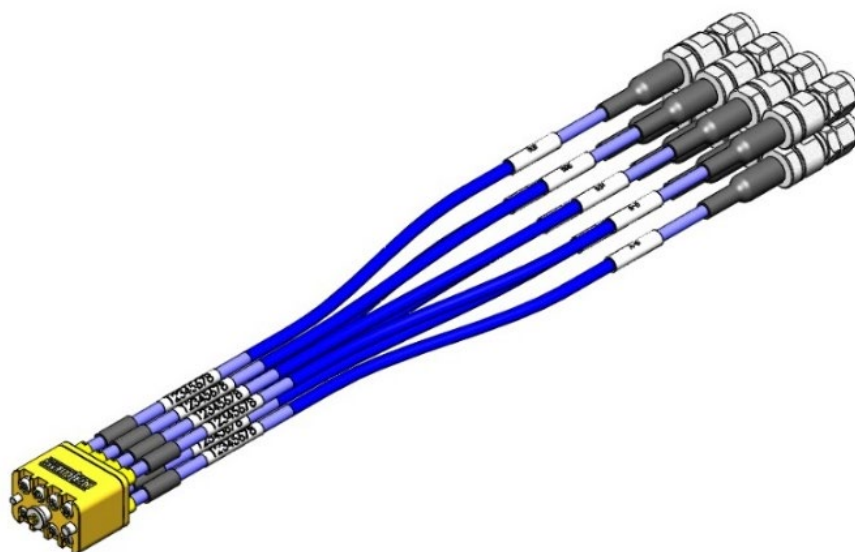




High Speed Characterization Report

BE70A-M-18SP-2-2-04-0152
BE70A-M-18SP-2-2-04-0305
BE70A-M-18SP-2-2-04-0610



Test Date: October 18, 2021

Description:
70 GHz, Bulls Eye® High-Performance Test Assembly,
Microstrip PCB Transmission

Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

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Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

Cable Assembly Overview

The BE70A series is a 4mm (0.157") pitch double row test assembly capable of supporting 70GHz (1.85 mm end option) frequency. The BE70A-M-18SP-2-2-04-XXXX series connector accepts 1.85 mm male and female test points. The data in this report is only applicable to BE70A connector mated to female 1.85 mm test points. The device under test contained 0.152-meter, 0.305-meter, and 0.610-meter cable samples.

Frequency Domain Data Summary

Test Parameter	Configuration	Driver	Receiver	152 mm	305 mm	610 mm
Insertion Loss	N/A	Test Board	BE70A	>-3.6dB@ 67 GHz	>-3.8dB@ 67 GHz	>-6.0dB@ 67 GHz
Return Loss	N/A	Test Board	BE70A	<-11.7dB to 60 GHz <-7.3dB to 67 GHz;	<-14.7dB to 60 GHz <-7.6dB to 67 GHz;	<-15.2dB to 60 GHz <-13.1dB to 67 GHz;
Isolation	NEXT	BE70A	BE70A	<-70.1dB to 67 GHz	Not Measured	Not Measured
	FEXT	BE70A	Test Board	<-62.4dB to 67 GHz	Not Measured	Not Measured

Time Domain Data Summary

Signal Rise-time	7.5ps	22.5ps	37.5ps	75ps
Maximum Impedance (Ohm)	51.5	51.3	51.2	51.1
Minimum Impedance (Ohm)	46.3	48.7	49.2	49.4

Series: BE70A-M

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Characterization Details

This report presents data that characterizes the signal integrity response of a connector cable assembly in a controlled printed circuit board (PCB) environment. All efforts are made to reveal typical best-case responses inherent to the system under test (SUT).

In this report, the SUT includes the connector cable assembly and footprint effects on a typical multi-layer PCB. PCB effects (trace loss) are de-embedded from test data. Board related effects, such as pad-to-ground capacitance, are included in the data presented in this report.

Additionally, intermediate test signal connections can mask the connector's true performance. Such connection effects are minimized by using high performance test cables and adapters. Where appropriate, calibration and de-embedding routines are also used to reduce residual effects.

Differential and Single-Ended Data

Most Samtec connectors can be used successfully in both differential and single-ended applications. However, electrical performance will differ depending on the signal drive type. In this report, data is presented for single-ended driven scenarios.

Signal Edge Speed (Rise Time):

In pulse signaling applications, the perceived performance of the interconnect can vary significantly depending on the edge rate or rise time of the exciting signal. For this report, the fastest rise time used was 7.5 ps. Generally, this should demonstrate worst-case performance.

In many systems, the signal edge rate will be significantly slower at the connector than at the driver launch point. To estimate interconnect performance at other edge rates, data is provided for several rise times between 7.5ps and 75ps.

For this report, measured rise times were at 20%-80% signal levels.

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Frequency Domain Data

Frequency Domain parameters are helpful in evaluating the connector system's signal loss and crosstalk characteristics across a range of sinusoidal frequencies. In this report, parameters presented in the Frequency Domain are Insertion Loss, Return Loss, and crosstalk. Other parameters or formats, such as VSWR or S-Parameters, may be available upon request. Please contact our Signal Integrity Group at sig@samtec.com for more information.

Frequency performance characteristics for the SUT are generated directly from network analyzer measurements.

Time Domain Data

Time Domain parameters indicate Impedance mismatch versus length, signal propagation time and crosstalk in a pulsed signal environment.

Impedance mismatch versus length is measured by a DSA8300 Digital Serial Analyzer. It can also be extracted from frequency domain data via transform. This report shows transformed time domain data. Board related effects, such as pad-to-ground capacitance and trace loss, are included in the data presented in this report. The impedance data is provided in [Appendix E](#) of this report.

The measured S-Parameters from the network analyzer are post-processed using Automatic Fixture Removal. The Time Domain procedure is provided in [Appendix E](#) of this report. Parameters or formats not included in this report may be available upon request. Please contact our Signal Integrity Group at sig@samtec.com for more information.

Data for other configurations may be available. Please contact our Signal Integrity Group at sig@samtec.com for further information.

Additional information concerning test conditions and procedures is located in the appendices of this report. Further information may be obtained by contacting our Signal Integrity Group at sig@samtec.com.

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Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

Appendix A – Frequency Domain Response Figures

Insertion Loss

BE70A-M-18SP-2-2-04-0152

Insertion Loss

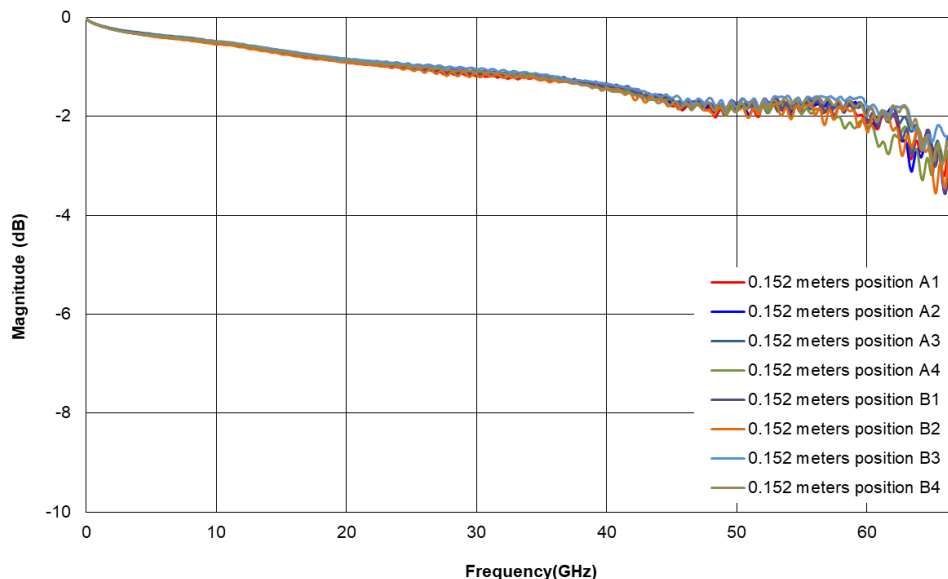


Figure 1

BE70A-M-18SP-2-2-04-0305

Insertion Loss

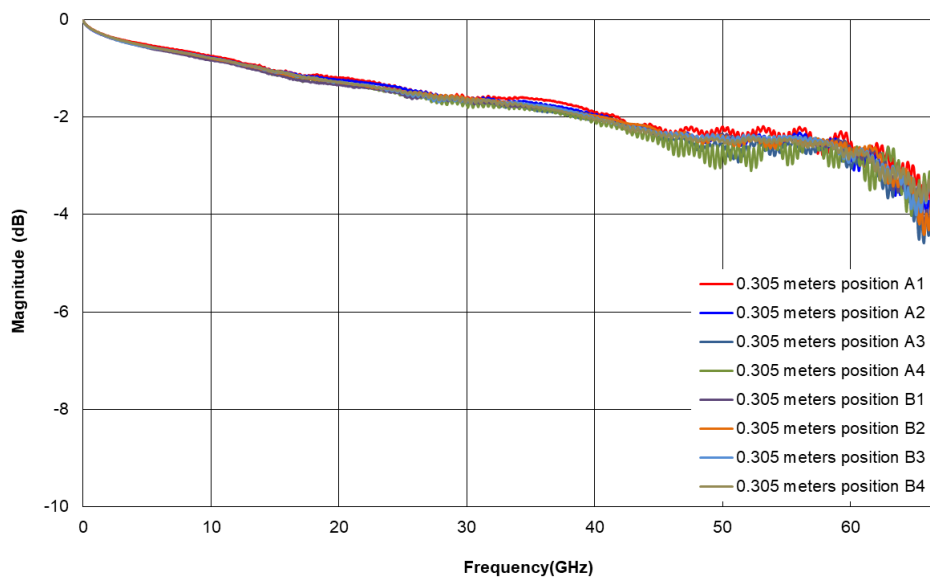
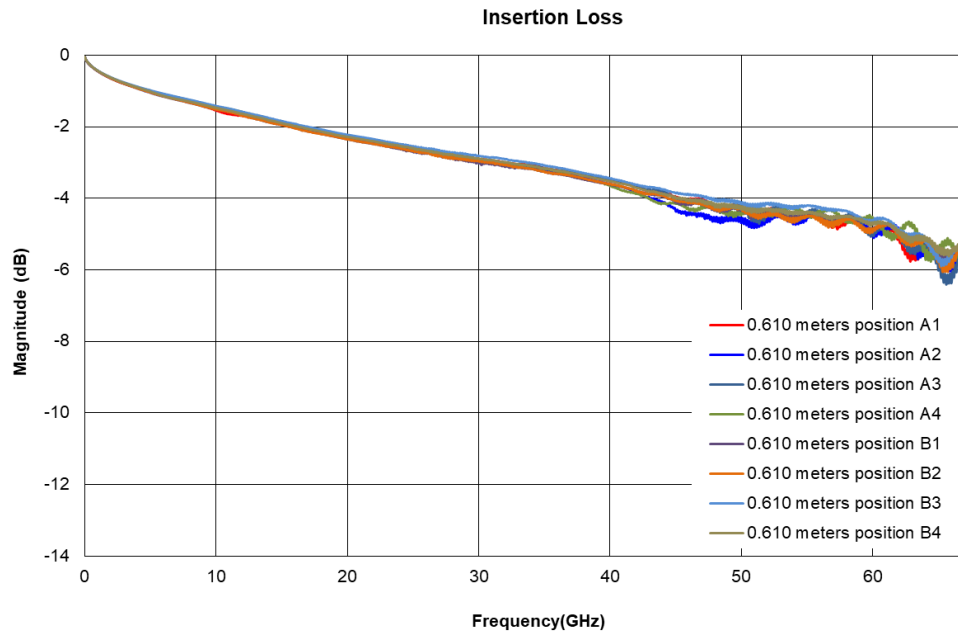
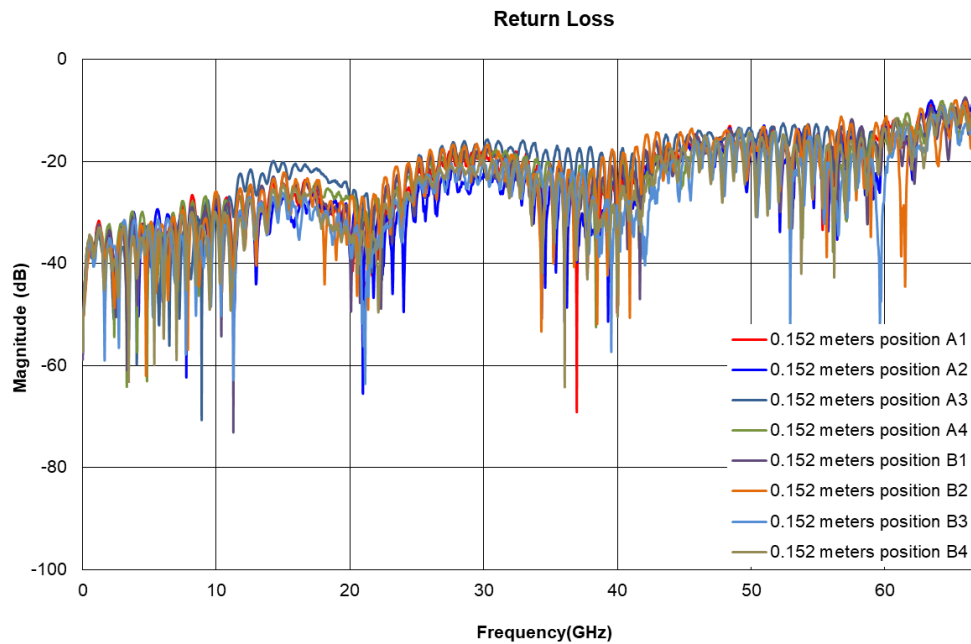


Figure 2

Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission**BE70A-M-18SP-2-2-04-0610****Figure 3****Return Loss****BE70A-M-18SP-2-2-04-0152****Figure 4**

Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

BE70A-M-18SP-2-2-04-0305

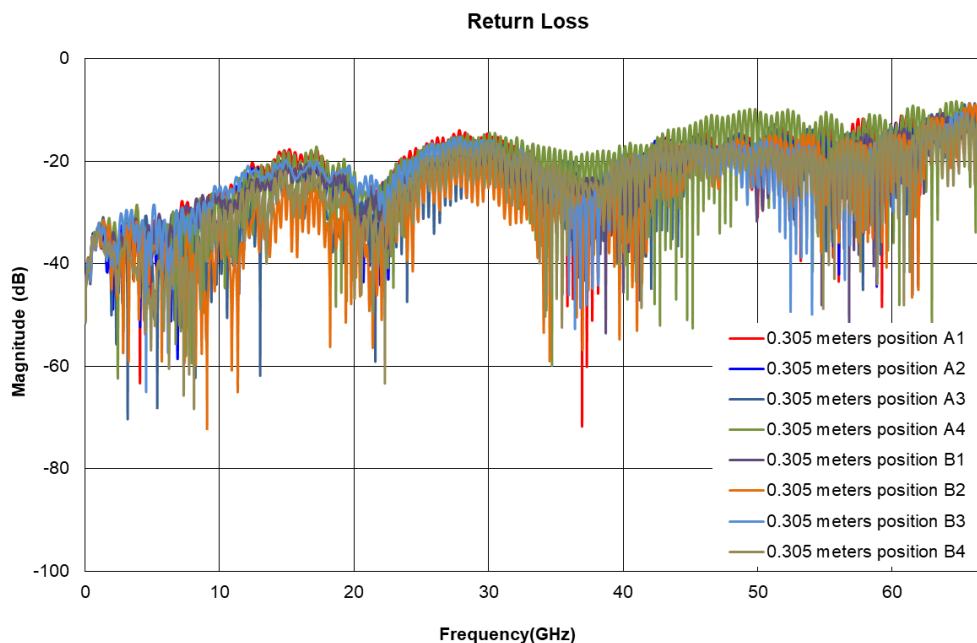


Figure 5

BE70A-M-18SP-2-2-04-0610

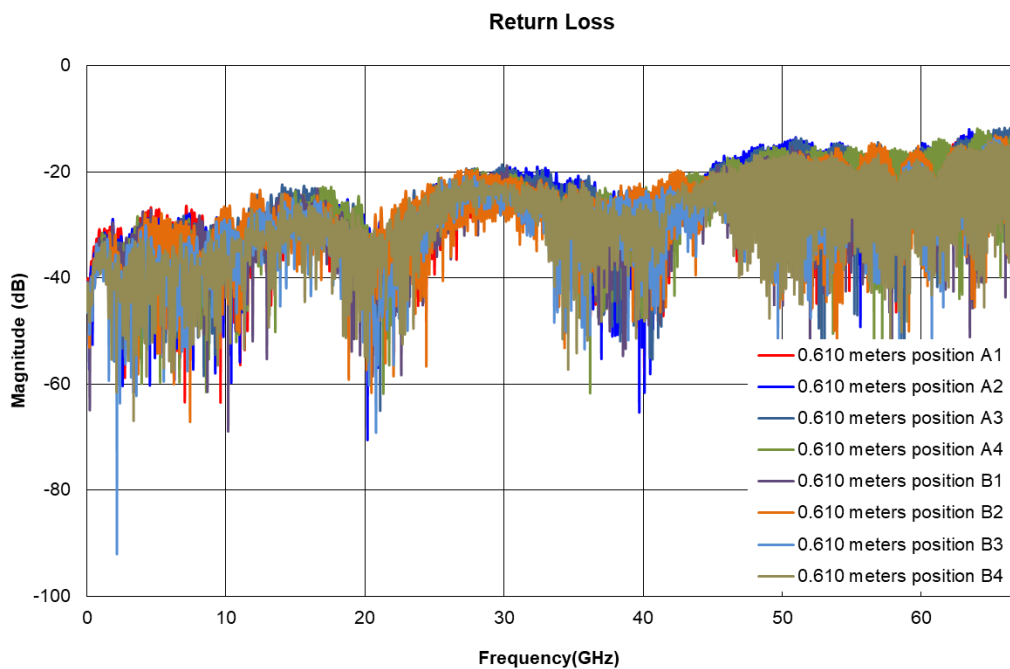


Figure 6

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

NEXT

BE70A-M-18SP-2-2-04-0152

Near End Crosstalk(NEXT)

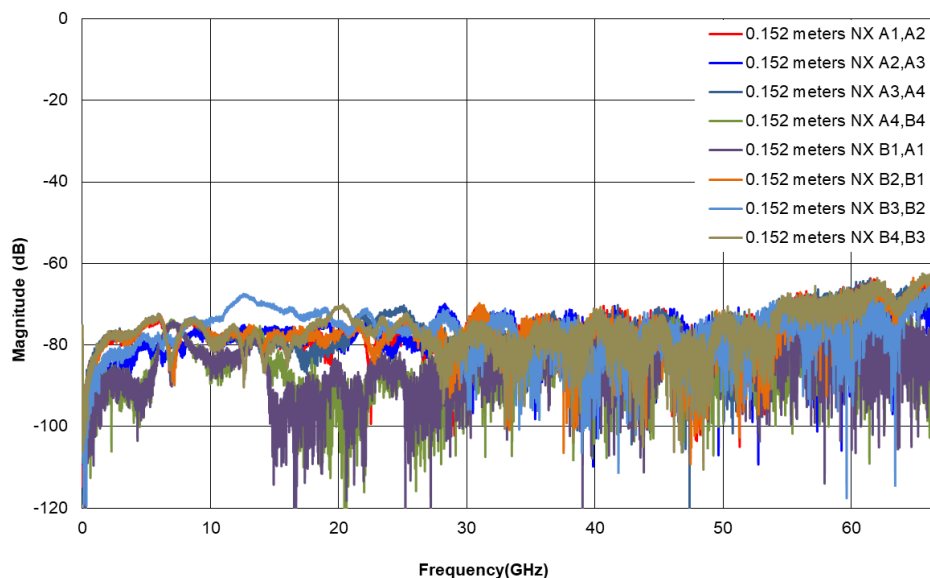


Figure 7

FEXT

BE70A-M-18SP-2-2-04-0152

Far End Crosstalk(FEXT)

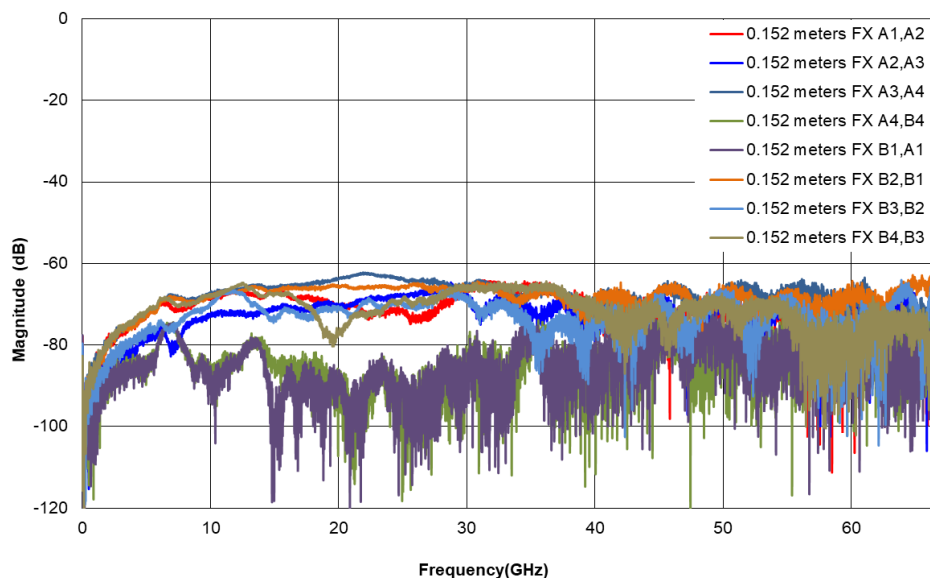


Figure 8

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

Appendix B – Time Domain Response Figures

Impedance

*All Impedance Profiles shown are of the 0.152 meters cable since the mating interfaces are identical.

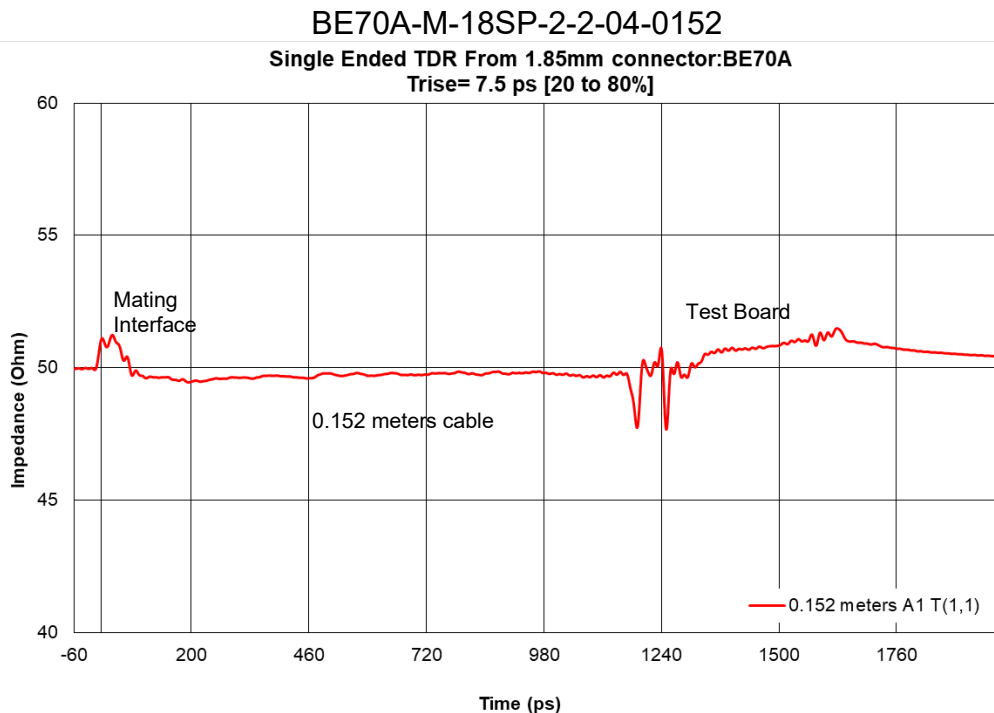


Figure 9

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

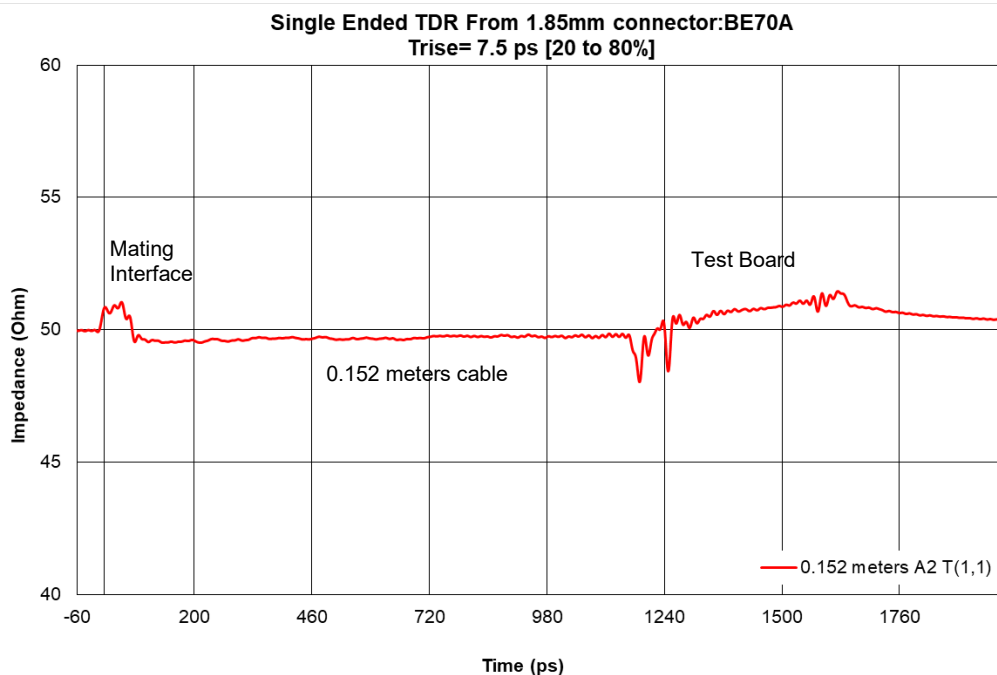


Figure 10

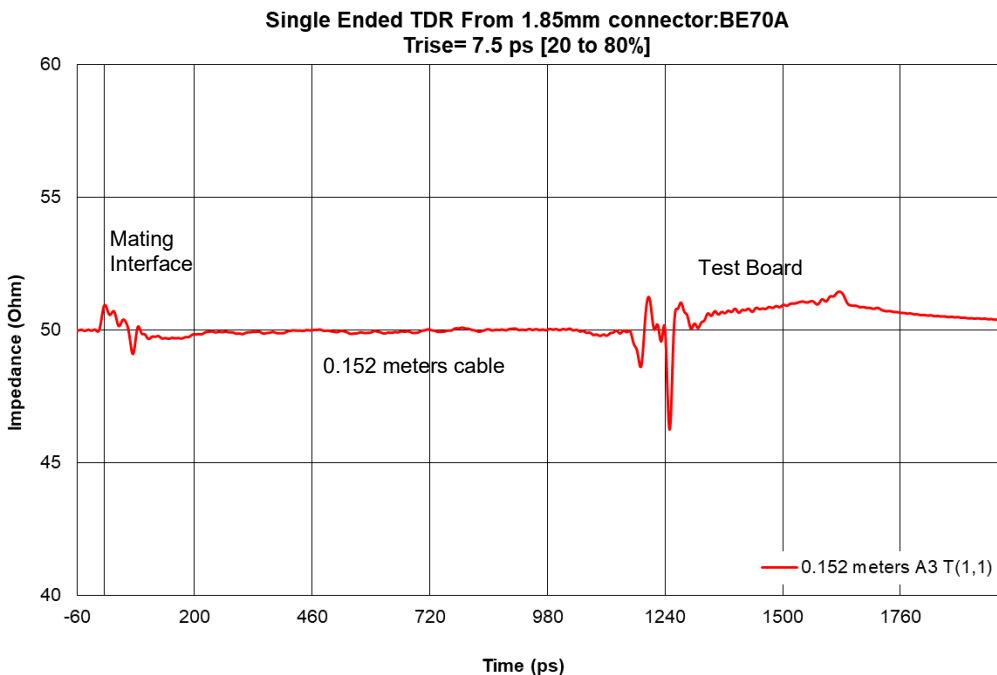
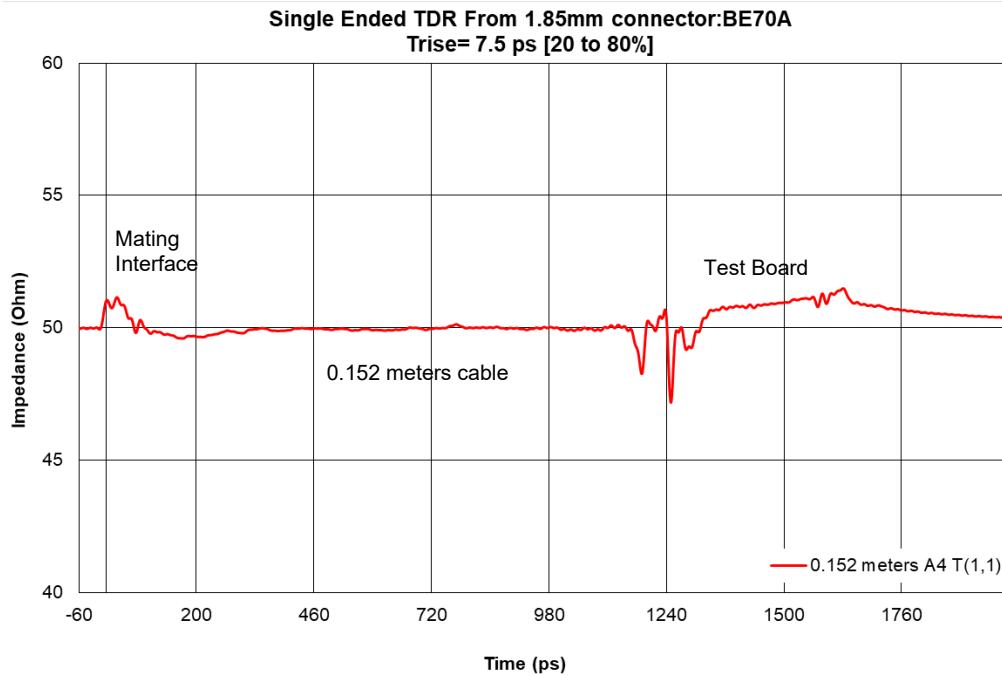
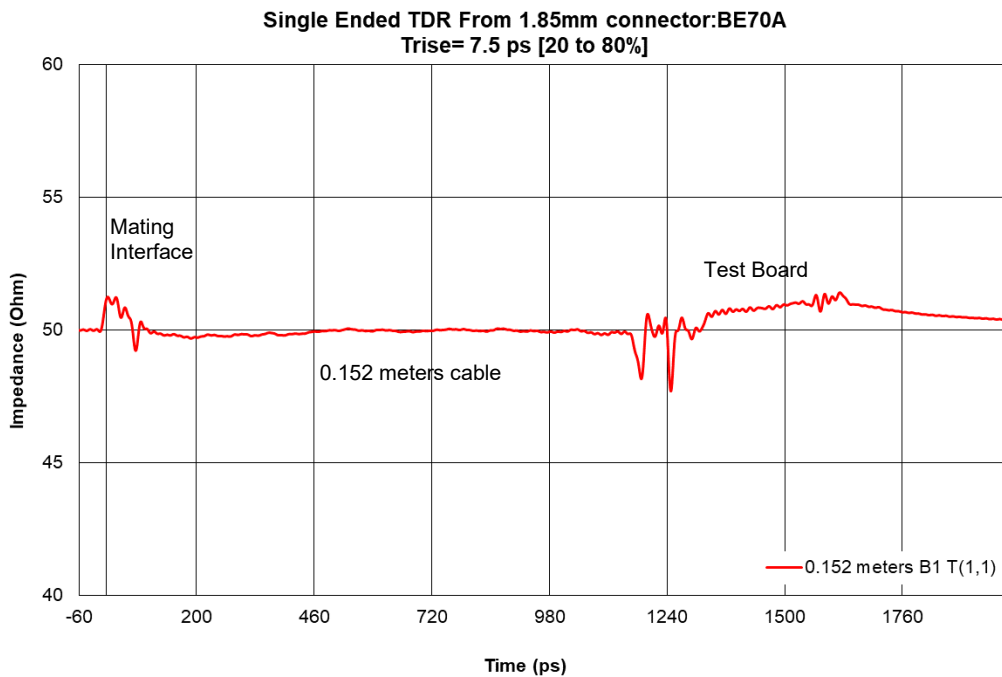


Figure 11

Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission**Figure 12****Figure 13**

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

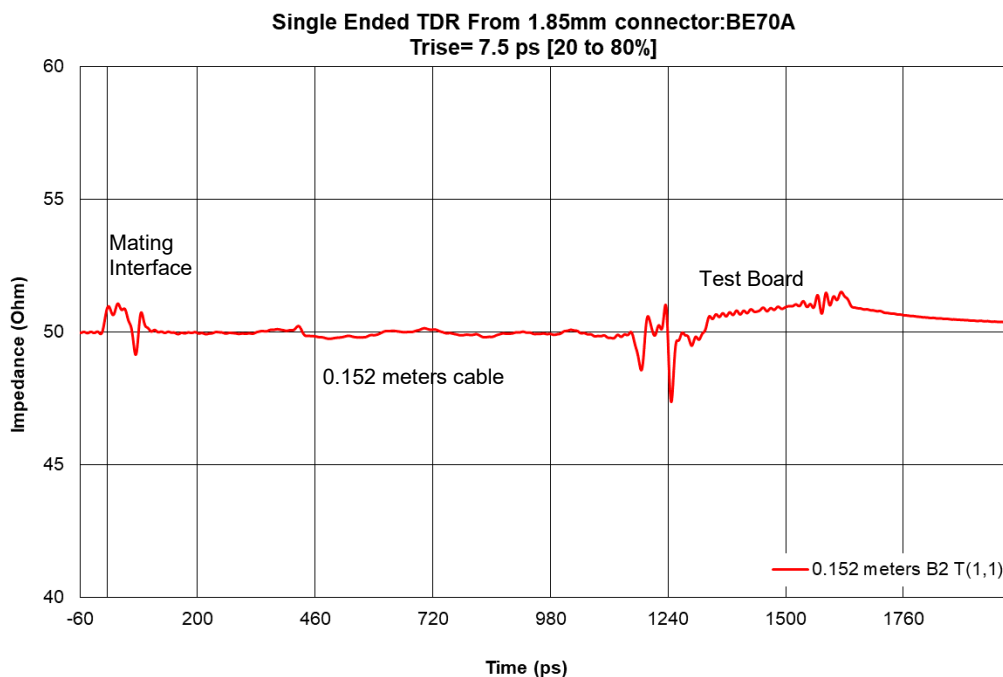


Figure 14

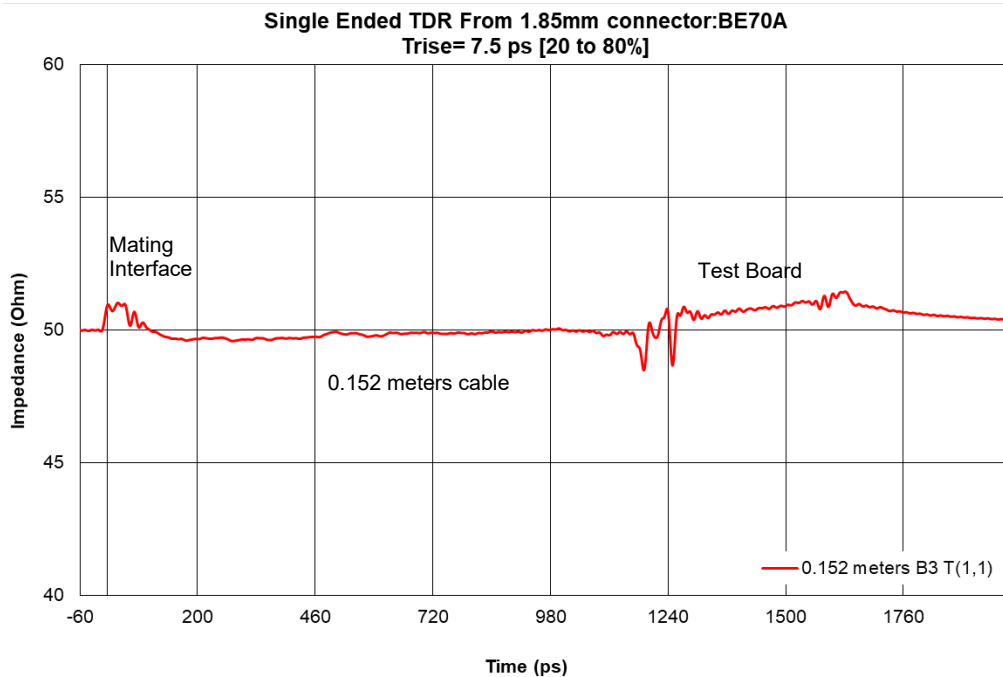
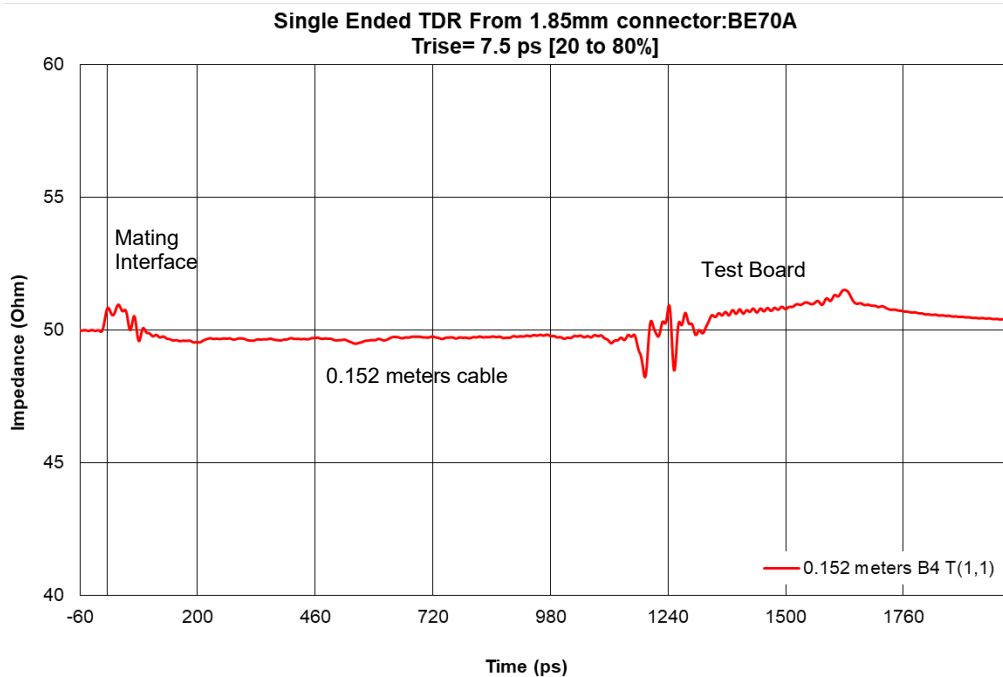


Figure 15

Series: BE70A-M**Description:** 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission**Figure 16**

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

Appendix C – Product and Test System Descriptions

Product Description

Product test samples are BE70A series connectors. The part number is BE70A-M-18SP-2-2-04-0152 for the 0.152 meters sample, BE70A-M-18SP-2-2-04-0305 for the 0.305 meters sample, and BE70A-M-18SP-2-2-04-0610 for the 0.610 meters sample. The BE70A Series is a high-speed double row test assembly. BE70A has two different ends, male 1.85mm test points, and the Bullseye housing. The part tested was made with 1.85mm straight plug male ends, and the Bullseye housing side was terminated to the 1.85mm female test points on the test board. A photo of the test points mounted to SI test boards is shown below.

Test System Description

The test fixture is composed of a Six-layer Isola Tachyon 100G material stack up with 50Ω signal trace and pad configurations designed for the electrical characterization of Samtec high speed connector products. A PCB mount 1.85 mm connector is used to interface the test cables to test fixtures. Optimization of the PCB mount 1.85 mm launch was performed using full wave simulation tools to minimize reflections. These launch designs were implemented on PCB-BE70MS-111183-SIG-1. Calibration standards specific to the BE70A series are located on the calibration traces on the aforementioned PCB.

PCB-BE70MS-111183-SIG-1 Test Fixture

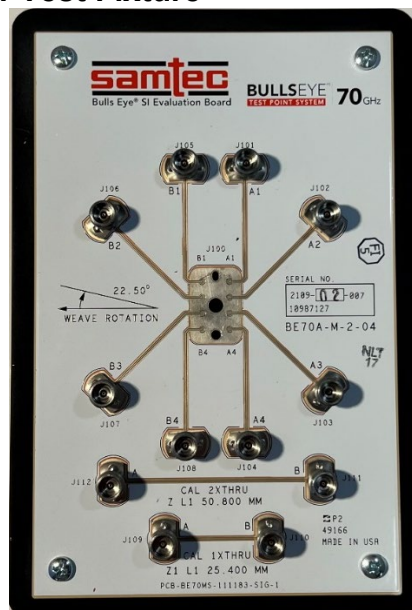


Figure 17

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

PCB-BE70MS-11183-SIG-1 Layout Panel

Artwork of the PCB design is shown below.

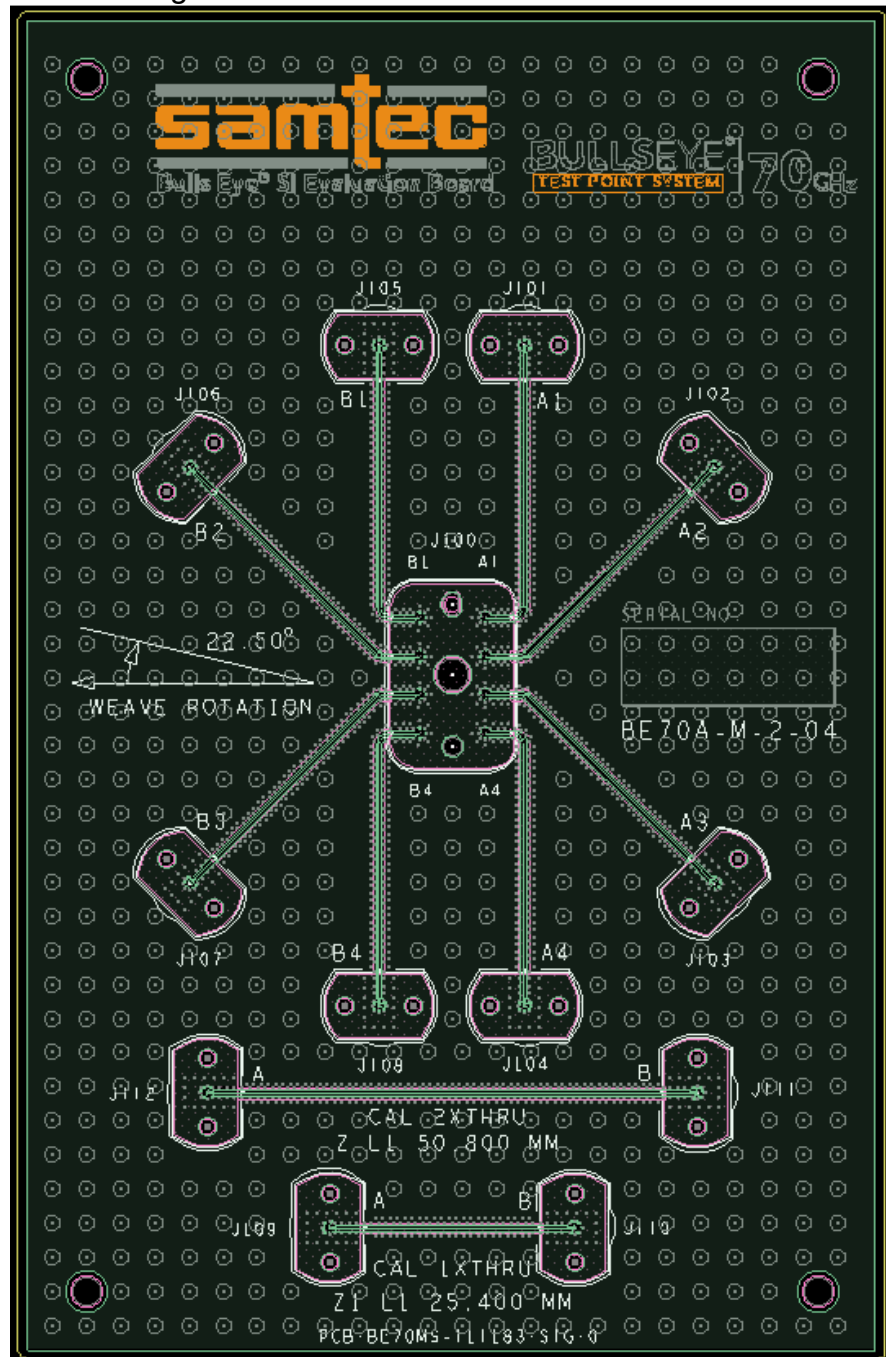


Figure 18

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

All traces on the test boards are length matched to the 1.85 mm PCB mount test connectors. The AFR application effectively removes most of test board trace effects. This means that 6.43 mm of test board trace length effects are included in the measurement. The S-Parameter measurement includes:

- A- The BE70A connector set
- B- Test board vias, pads (footprint effects) for the BE70A connector side
- C- 6.43 mm of microstrip trace
- D- Test board vias, pads (footprint effects)

The figure below shows the location of the reference plane performing AFR application.

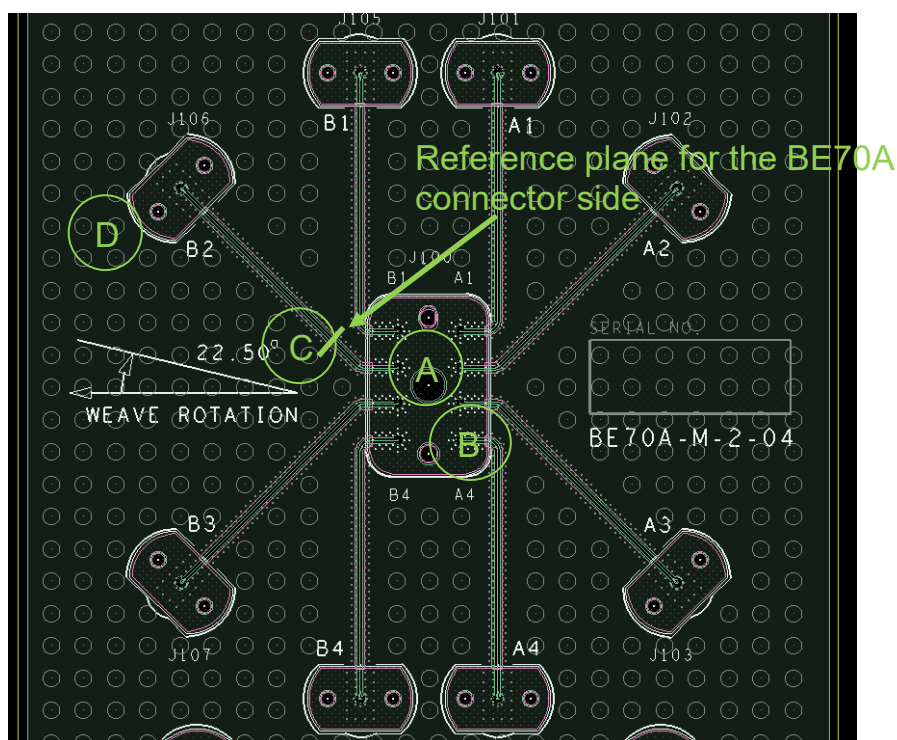


Figure 19

Series: BE70A-M

Description: 70 GHz, Bulls Eye® High-Performance Test Assembly, Microstrip PCB Transmission

Appendix D – Test and Measurement Setup

For frequency domain measurements, the test instrument is the Keysight N5227A PNA network analyzer. Frequency domain data and graphs are extracted from the instrument by AFR application. Time domain data was not de-embedded and is a raw measurement of the system. The network analyzer is configured as follows:

Start Frequency – 10 MHz

Stop Frequency – 67 GHz

Step Size –10 MHz

IFBW –200 Hz

N5227A Measurement Setup

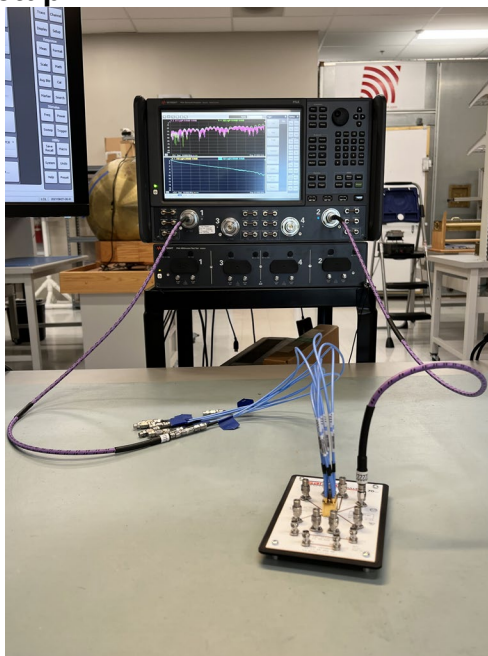


Figure 20

Test Instruments

<u>QTY</u>	<u>Description</u>
1	Keysight N5227A PNA-L Network Analyzer (10 MHz to 67 GHz)
1	Keysight 85058B E Cal module (10MHz to 67 GHz)

Test Cables & Adapters

<u>QTY</u>	<u>Description</u>
4	Gore 1.85mm male to female test cables (DC-67 GHz)

Series: BE70A-M

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Appendix E - Frequency and Time Domain Measurements

Frequency (S-Parameter) Domain Procedures

The quality of any data taken with a network analyzer is directly related to the quality of the calibration standards and the use of proper test procedures. For this reason, extreme care is taken in the design of the through calibration standards, the SI test boards, and the selection of the PCB vendor.

A coaxial SOLT calibration is performed using 85058B 1.85mm electronic calibration kit. Then DUT measurements are performed under SOLT calibration. The measurements include the effect of test fixture. The measurements of the 2X THRU line standards are required to remove the test fixture effect.

Time Domain Procedures

Mathematically, Frequency Domain data can be transformed to obtain a Time Domain response. Perfect transformation requires Frequency Domain data from DC to infinity Hz. Fortunately, a very accurate Time Domain response can be obtained with bandwidth-limited data, such as measured with modern network analyzer.

The time domain data in this report was taken from time domain transforms of frequency domain data. An example of a transform methodology is provided in the Samtec Technical Note on domain transformation. http://www.samtec.com/Technical_Library/reference/articles/pdfs/tech-note_using-PLTS-for-time-domain-data_web.pdf

Impedance (TDR)

Measurements involving digital pulses are performed using either Time Domain Reflectometer (TDR) or Time Domain Transmission (TDT) methods. The TDR method is used for the impedance measurements in this report.

The signal line(s) of the SUT's is energized with a TDR pulse and the far-end of the energized signal line is terminated in the test systems characteristic impedance (e.g., 50Ω or 100Ω terminations). By terminating the adjacent signal lines in the test systems characteristic impedance, the effects on the resultant impedance shape of the waveform are limited. The "best case" signal mapping was evaluated and is presented in this report.

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Appendix F – Glossary of Terms

AFR – Automatic Fixture Removal

DUT – Device under test

FD – Frequency domain

FEXT – Far-End Crosstalk

NEXT – Near-End Crosstalk

PCB – Printed Circuit Board

SOLT – Acronym used to define Short, Open, Load & Thru Calibration Standards

SUT – System Under Test

TDR – Time Domain Reflectometry

VSWR – Voltage Standing Wave Ratio

Z – Impedance (expressed in ohms)