

Series: FTE/CLE

Description: Surface Mount, 0.8mm (.0315") Pitch, 5mm (0.197") Stack Height

## Frequency Domain Data Summary

Table 1 - Single-Ended Signaling System Performance				
Test Parameter	Filename	Source	Victim	
Insertion Loss	SL_1_1	Tx, port1=FTE_44, Rx, port3=CLE_44		-3dB @ 8.1 GHz
Return Loss	SL_1_1	Tx, port1=FTE_44, Rx, port3=CLE_44		≤ -5dB to 8.1 GHz
Near-End Crosstalk	SN_1_1	FTE_40	FTE_44	≤ -7dB to 8.1 GHz
	SN_1_2	FTE_42	FTE_40	≤ -25dB to 8.1 GHz
	SN_1_3	FTE_7	FTE_8	≤ -20dB to 8.1 GHz
Far-End Crosstalk	SF_1_1	FTE_40	CLE_44	≤ -12dB to 8.1 GHz
	SF_1_2	FTE_42	CLE_40	≤ -22dB to 8.1 GHz
	SF_1_3	FTE_7	CLE_8	≤ -21dB to 8.1 GHz

Table 2 - Differential Signaling System Performance				
Test Parameter	Filename	Source	Victim	
Insertion Loss	DL_1_1	Tx, port12=FTE_90-92, Rx, port34=CLE_90-92		-3dB @ 8.8GHz
Return Loss	DL_1_1	Tx, port12=FTE_90-92, Rx, port34=CLE_90-92		≤ -10dB to 8.8 GHz
Near-End Crosstalk	DN_1_1	FTE_94-92	FTE_90-88	≤ -19dB to 8.8 GHz
	DN_1_2	FTE_84-86	FTE_90-92	≤ -42dB to 8.8 GHz
	DN_1_3	FTE_6-4	FTE_3-5	≤ -29dB to 8.8 GHz
Far-End Crosstalk	DF_1_1	FTE_94-92	CLE_90-88	≤ -22dB to 8.8 GHz
	DF_1_2	FTE_84-86	CLE_90-92	≤ -29dB to 8.8 GHz
	DN_1_3	FTE_6-4	CLE_3-5	≤ -37dB to 8.8 GHz

Pin Map (reference Appendix C for full description of test boards)

Series: FTE/CLE

Description: Surface Mount, 0.8mm (.0315") Pitch, 5mm (0.197") Stack Height

### Bandwidth Chart – Single-Ended & Differential Insertion Loss

CLE/FTE Socket & Terminal Strip Series

