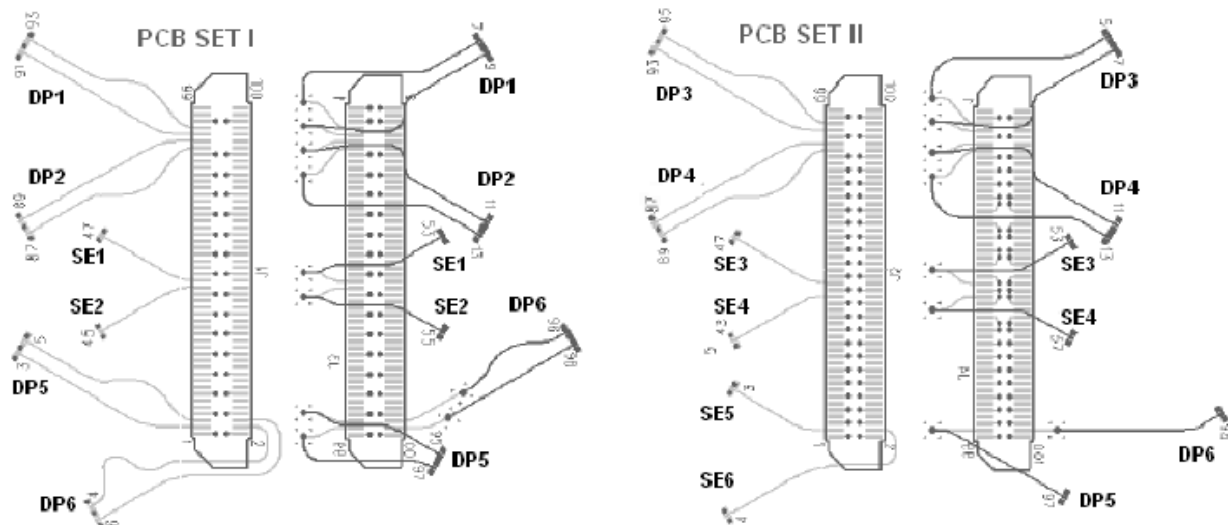


Series: Terminal/Socket LSS Series
Description: 0.635mm (.025") Pitch, 6mm (0.2365") Stack Height

Frequency Domain Data Summary

Table 1 - Single-Ended Signaling System Performance				
Test Parameter	Drive/Listen	Source	Victim	
Insertion Loss	SE3	port 1=LSS_47 port 2=LSS_53		-3dB @ 9.8 GHz
Return Loss	SE3	port 1=LSS_47 port 2=LSS_53		≤ -3dB to 9.8 GHz
Near-End Crosstalk	SE2 to SE1	LSS_45	LSS_47	≤ -5dB to 9.8 GHz
	SE4 to SE3	LSS_43	LSS_47	≤ -15dB to 9.8 GHz
	SE6 to SE5	LSS_4	LSS_3	≤ -22dB to 9.8 GHz
Far-End Crosstalk	SE2 to SE1	LSS_45	LSS_53	≤ -5dB to 9.8 GHz
	SE4 to SE3	LSS_43	LSS_53	≤ -15dB to 9.8 GHz
	SE6 to SE5	LSS_4	LSS_97	≤ -25dB to 9.8 GHz

Pin Map (reference Appendix C for full description of test boards)



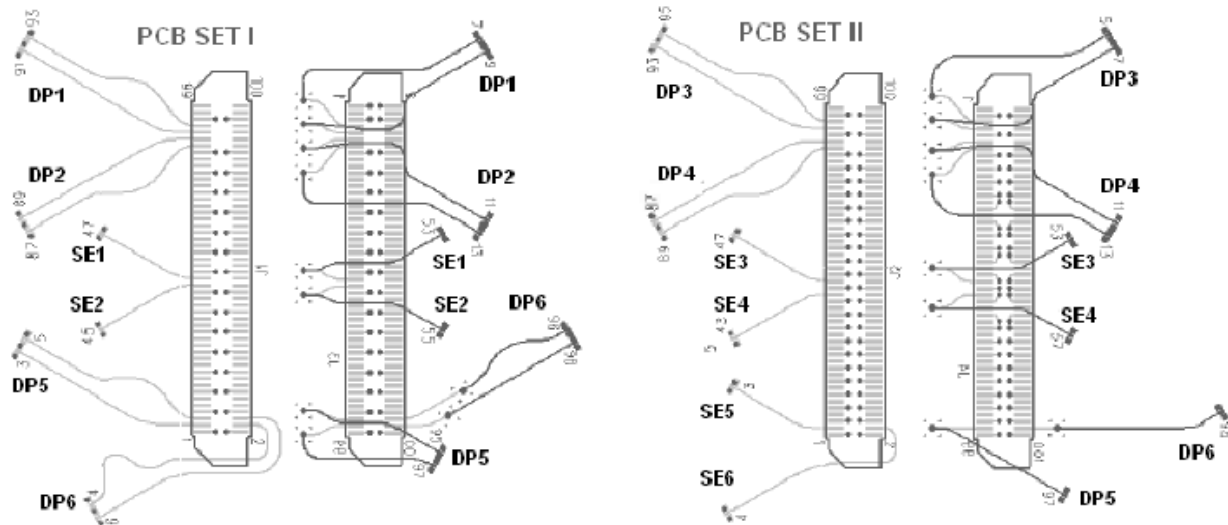
Series: Terminal/Socket LSS Series

Description: 0.635mm (.025") Pitch, 6mm (0.2365") Stack Height

Table 2 - Differential Signaling System Performance

Test Parameter	Signal Line(s)	Source	Victim	
Insertion Loss	DP3	port 1=LSS_93-95 port 2=LSS_5-7		-3dB @ 8.9 GHz
Return Loss	DP3	port 1=LSS_93-95 port 2=LSS_5-7		≤ -5dB to 8.9 GHz
Near-End Crosstalk	DP2 to DP1	LSS_87-89	LSS_91-93	≤ -18dB to 8.9 GHz
	DP4 to DP3	LSS_87-89	LSS_93-95	≤ -28dB to 8.9 GHz
	DP6 to DP5	LSS_4-6	LSS_3-5	≤ -35dB to 8.9 GHz
Far-End Crosstalk	DP2 to DP1	LSS_87-89	LSS_7-9	≤ -25dB to 8.9 GHz
	DP4 to DP3	LSS_87-89	LSS_5-7	≤ -25dB to 8.9 GHz
	DP6 to DP5	LSS_4-6	LSS_95-97	≤ -35dB to 8.9 GHz

Pin Map (reference Appendix C for full description of test boards)



Series: Terminal/Socket LSS Series

Description: 0.635mm (.025") Pitch, 6mm (0.2365") Stack Height

Bandwidth Chart – Single-Ended & Differential Insertion Loss

Locking Terminal/Socket Strip Connector

