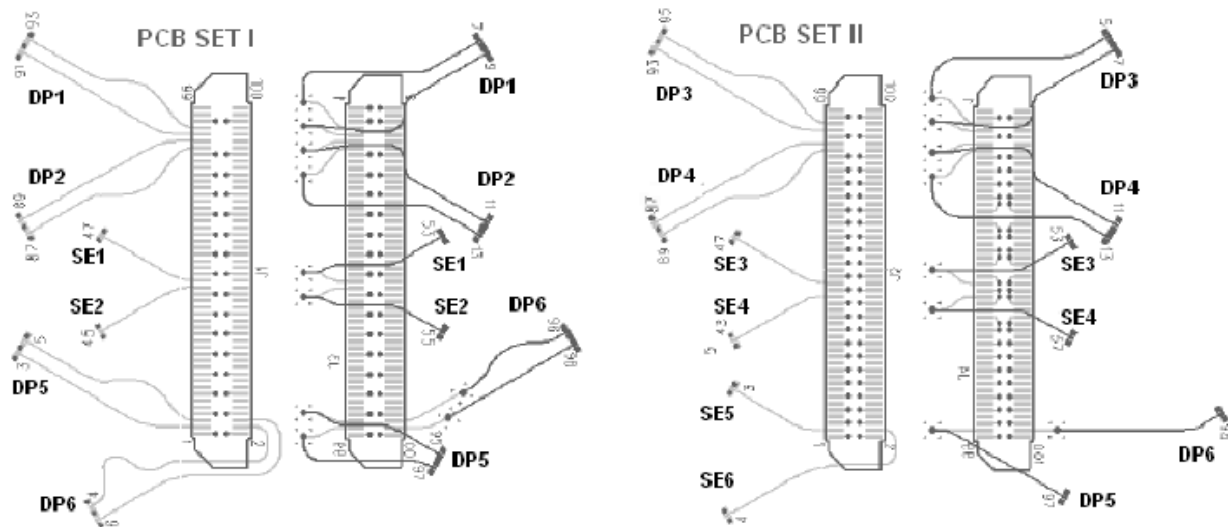


**Series:** Terminal/Socket LSS Series  
**Description:** 0.635mm (.025") Pitch, 12mm (0.4724") Stack Height

## Frequency Domain Data Summary

| Table 1 - Single-Ended Signaling System Performance |              |                                |        |                    |
|-----------------------------------------------------|--------------|--------------------------------|--------|--------------------|
| Test Parameter                                      | Drive/Listen | Source                         | Victim |                    |
| Insertion Loss                                      | SE3          | port 1=LSS_47<br>port 2=LSS_53 |        | ≤ -3dB to 6.9 GHz  |
| Return Loss                                         | SE3          | port 1=LSS_47<br>port 2=LSS_53 |        | ≤ -5dB to 6.9 GHz  |
| Near-End Crosstalk                                  | SE2 to SE1   | LSS_45                         | LSS_47 | ≤ -15dB to 6.9 GHz |
|                                                     | SE4 to SE3   | LSS_43                         | LSS_47 | ≤ -25dB to 6.9 GHz |
|                                                     | SE6 to SE5   | LSS_4                          | LSS_3  | ≤ -15dB to 6.9 GHz |
| Far-End Crosstalk                                   | SE2 to SE1   | LSS_45                         | LSS_53 | ≤ -20dB to 6.9 GHz |
|                                                     | SE4 to SE3   | LSS_43                         | LSS_53 | ≤ -32dB to 6.9 GHz |
|                                                     | SE6 to SE5   | LSS_4                          | LSS_97 | ≤ -3dB to 6.9 GHz  |

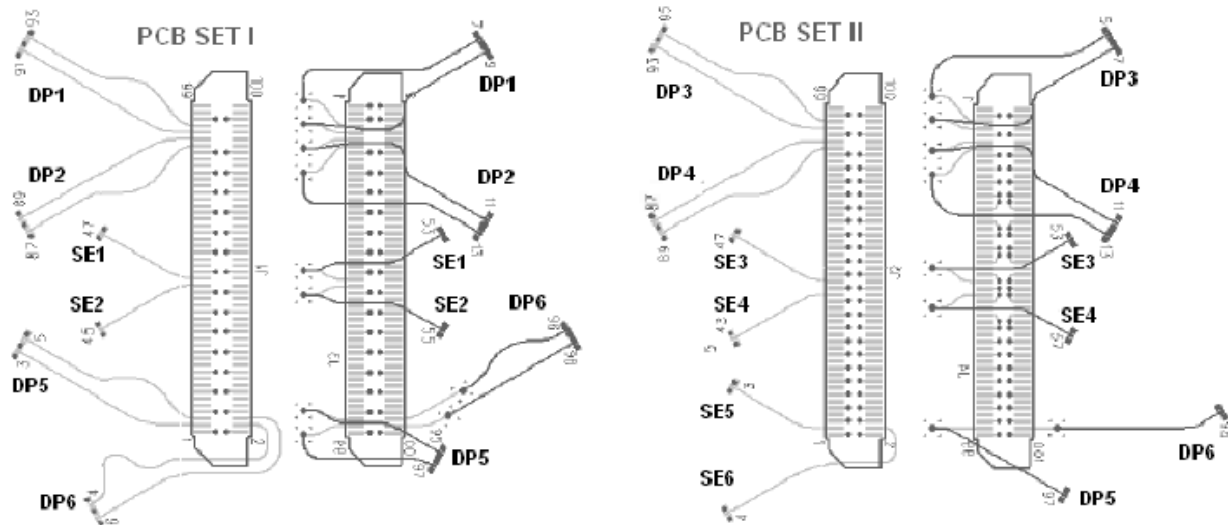
Pin Map (reference Appendix C for full description of test boards)



**Series:** Terminal/Socket LSS Series  
**Description:** 0.635mm (.025") Pitch, 12mm (0.4724") Stack Height

| Table 2 - Differential Signaling System Performance |                |                                    |           |                    |
|-----------------------------------------------------|----------------|------------------------------------|-----------|--------------------|
| Test Parameter                                      | Signal Line(s) | Source                             | Victim    |                    |
| Insertion Loss                                      | DP3            | port 1=LSS_93-95<br>port 2=LSS_5-7 |           | -3dB @ 6.4 GHz     |
| Return Loss                                         | DP3            | port 1=LSS_93-95<br>port 2=LSS_5-7 |           | ≤ -3dB to 6.4 GHz  |
| Near-End Crosstalk                                  | DP2 to DP1     | LSS_87-89                          | LSS_91-93 | ≤ -18dB to 6.4 GHz |
|                                                     | DP4 to DP3     | LSS_87-89                          | LSS_93-95 | ≤ -32dB to 6.4 GHz |
|                                                     | DP6 to DP5     | LSS_4-6                            | LSS_3-5   | ≤ -35dB to 6.4 GHz |
| Far-End Crosstalk                                   | DP2 to DP1     | LSS_87-89                          | LSS_7-9   | ≤ -25dB to 6.4 GHz |
|                                                     | DP4 to DP3     | LSS_87-89                          | LSS_5-7   | ≤ -32dB to 6.4 GHz |
|                                                     | DP6 to DP5     | LSS_4-6                            | LSS_95-97 | ≤ -35dB to 6.4 GHz |

Pin Map (reference Appendix C for full description of test boards)



**Series:** Terminal/Socket LSS Series

**Description:** 0.635mm (.025") Pitch, 12mm (0.4724") Stack Height

## Bandwidth Chart – Single-Ended & Differential Insertion Loss

Locking Terminal/Socket Strip Connector

