

Series: QMSS-DP/QFSS-DP

Description: Shielded Q2 Socket/Terminal Strip, GSSG Differential Pair Shield Configuration, 0.635mm (.025") Pitch, 11mm (.433") Stack Height

Frequency Domain Data Summary

Table 1 - Differential Pair Signaling - System Performance				
Test Parameter	File	Source	Victim	
Insertion Loss	RLIL_P12_93_95_P34_93_95	port12=QFSS-DP_93-95, port34=QMSS-DP_93-95		-3dB @ 7.5 GHz
Return Loss	RLIL_P12_93_95_P34_93_95	port12=QFSS-DP_93-95, port34=QMSS-DP_93-95		≤ -3dB to 7.5 GHz
Near-End Crosstalk	NV1_P12_93_95_P34_99_101	QFSS-DP_93-95	QFSS-DP_99-101	≤ -25dB to 7.5 GHz
	NV2_P12_93_95_P34_94_96	QFSS-DP_93-95	QFSS-DP_94-96	≤ -58dB to 7.5 GHz
Far-End Crosstalk	FV1_P12_93_95_P34_99_101	QFSS-DP_93-95	QMSS-DP_99-101	≤ -24dB to 7.5 GHz
	FV2-P12_93_95_P34_94_96	QFSS-DP_93-95	QMSS-DP_94-96	≤ -55dB to 7.5 GHz

Bandwidth Chart –DP Version & Standard Differential Version

