

Series: TOLC/SOLC, 1.27mm (0.050") Contact Pitch, 0.64mm (0.025") Footprint Pitch

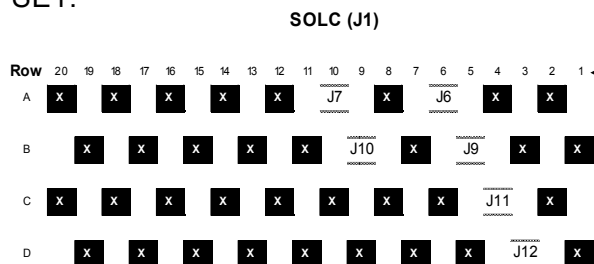
Description:, Board-to-Board, Surface Mount, 12mm (0.472") Stack Height

Frequency Domain Data Summary

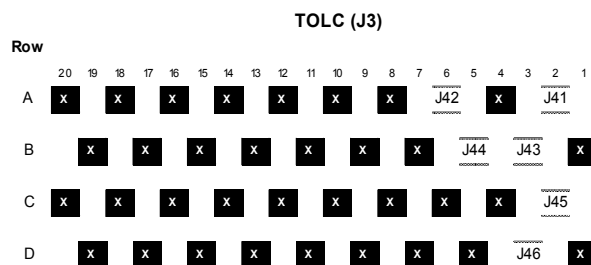
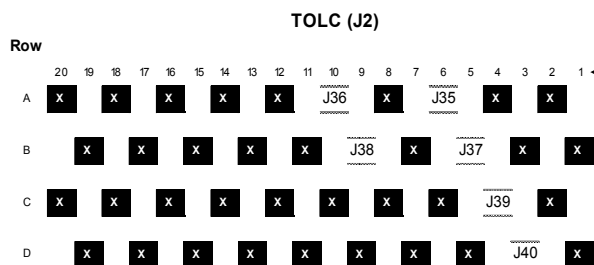
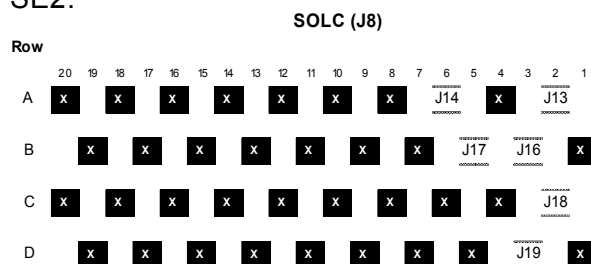
Table 1 - Single-Ended Connector System Performance			
Test Parameter	Source	Victim	
Insertion Loss	port1=J6; port2=J35		-3dB @ 5.16 GHz
Return Loss	port1=J6; port2=J35		≤ -5 dB to 5.16 GHz
Near-End Crosstalk	J6 (SE1)	J9 (SE1)	≤ -12dB to 5.16 GHz
	J9 (SE1)	J11 (SE1)	≤ -8dB to 5.16 GHz
	J16 (SE2)	J17 (SE2)	≤ -12dB to 5.16 GHz
	J6 (SE1)	J7 (SE1)	≤ -25dB to 5.16 GHz
Far-End Crosstalk	J6 (SE1)	J37 (SE1)	≤ -8dB to 5.16 GHz
	J9 (SE1)	J39 (SE1)	≤ -22dB to 5.16 GHz
	J16 (SE2)	J44 (SE2)	≤ -25dB to 5.16 GHz
	J6 (SE1)	J36 (SE1)	≤ -18dB to 5.16 GHz

Pin Map (reference Appendix C for full description of test boards)

SE1:



SE2:

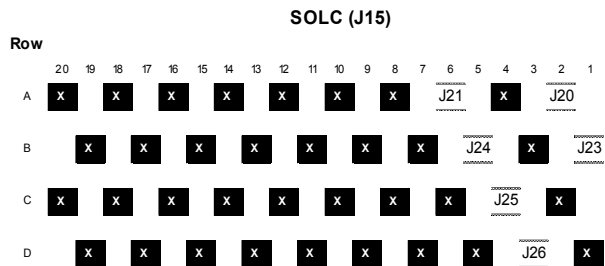


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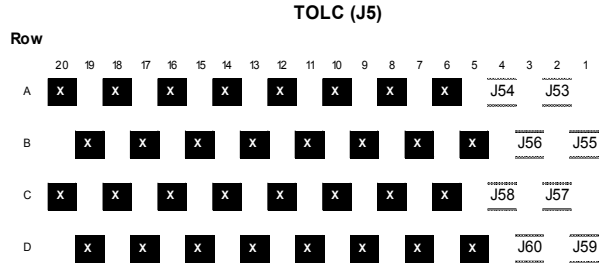
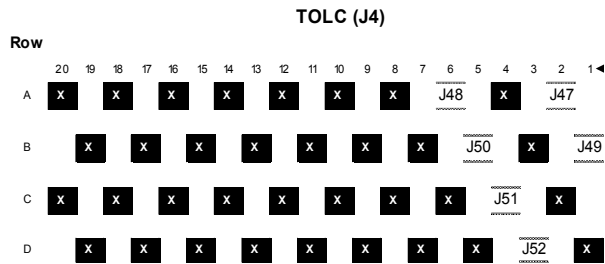
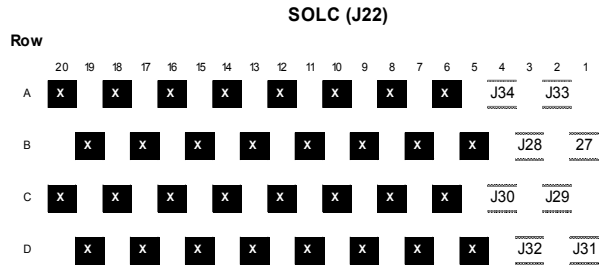
Test Parameter	Source	Victim	
Insertion Loss	Port1= J21-J24		-3dB @ 5.11 GHz
	Port 2 = J48-J50		
Return Loss	Port1= J21-J24		≤ -5dB to 5.11 GHz
	Port 2 = J48-J50		
Near-End Crosstalk	J34-J28 (DP2)	J33-J27 (DP2)	≤ -12dB to 5.11 GHz
	J21-J24 (DP1)	J25-J26 (DP1)	≤ -18dB to 5.11 GHz
	J14-J17 (SE2)	J13-J16 (SE2)	≤ -18dB to 5.11 GHz
	J34-J28 (DP2)	J30-J32 (DP2)	≤ -22dB to 5.11 GHz
	J21-J24 (DP1)	J20-J23 (DP1)	≤ -30dB to 5.11 GHz
Far-End Crosstalk	J34-J28 (DP2)	J53-J55 (DP2)	≤ -15dB to 5.11 GHz
	J21-J24 (DP1)	J51-J52 (DP1)	≤ -25dB to 5.11 GHz
	J14-J17 (SE2)	J41-J43 (SE2)	≤ -22dB to 5.11 GHz
	J34-J28 (DP2)	J58-J60 (DP2)	≤ -18dB to 5.11 GHz
	J21-J24 (DP1)	J47-J49 (DP1)	≤ -28dB to 5.11 GHz

Pin Map (reference Appendix C for full description of test boards)

DP1:



DP2:



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Bandwidth Chart - Single-Ended & Differential Insertion Loss

PCB/Connector Test System
Single Ended & Differential Signal Response
SOLC/TOLC Quad Row Series

