

Series: HSEC8-DV

Description: High Speed Edge Card Socket, Vertical Surface Mount,
0.8mm (0.0315") Pitch, Mated with 1.60mm (.062") thick load card

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)					
Signal Rise-time	30ps	50ps	100ps	250ps	500ps
Maximum Impedance	58.1	52.7	52.4	51.6	51.2
Minimum Impedance	43.4	46.4	48.8	49.6	50.0

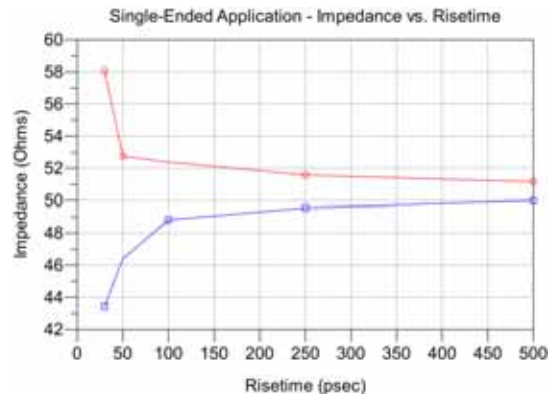
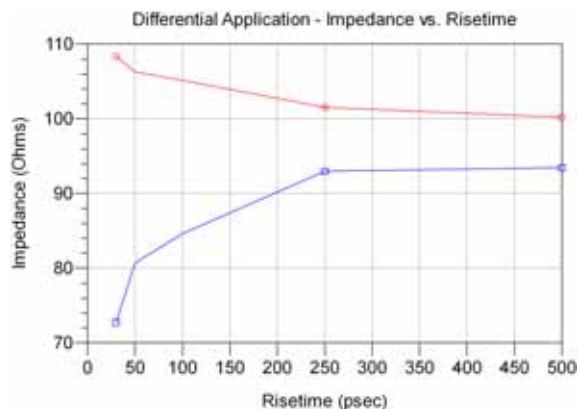


Table 4 - Differential Impedance (Ω)					
Signal Rise-time	30ps	50ps	100ps	250ps	500ps
Maximum Impedance	108.3	106.2	105.1	101.5	100.2
Minimum Impedance	72.7	80.7	84.6	93.0	93.4



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Table 5 - Single-Ended Crosstalk (%)						
Input(t _r)		30ps	50ps	100ps	250ps	500ps
NEXT	GAQG	19.27	17.47	12.97	6.19	3.12
	GAGQG	2.82	2.26	1.47	0.66	0.33
	Xrow	2.22	1.86	1.18	0.53	0.29
FEXT	GAQG	5.25	3.62	2.29	1.03	0.66
	GAGQG	2.54	1.59	0.71	0.25	0.18
	Xrow	0.28	0.20	0.10	<0.1	<0.1

Table 6 - Differential Crosstalk (%)						
Input(t _r)		30ps	50ps	100ps	250ps	500ps
NEXT	GAAQQG	5.27	5.00	3.90	1.99	1.05
	GAAGQQG	0.47	0.36	0.23	0.11	<0.1
	Xrow	0.56	0.44	0.26	0.12	<0.1
FEXT	GAAQQG	0.99	0.86	0.49	0.16	<0.1
	GAAGQQG	0.53	0.34	0.13	<0.1	<0.1
	Xrow	0.13	0.10	<0.1	<0.1	<0.1

Table 7 - Propagation Delay (Mated Connector)	
Single-Ended	44 ps
Differential	40 ps