

Series: AW/CLE

Description: Surface Mount, 0.8mm (.0315") Pitch, 9.14mm (0.360") Stack Height

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)							
Signal Risetime	35±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	61.7	56.9	53.0	51.6	51.3	51.2	51.1
Minimum Impedance	41.0	46.2	48.8	49.1	49.1	49.2	49.3

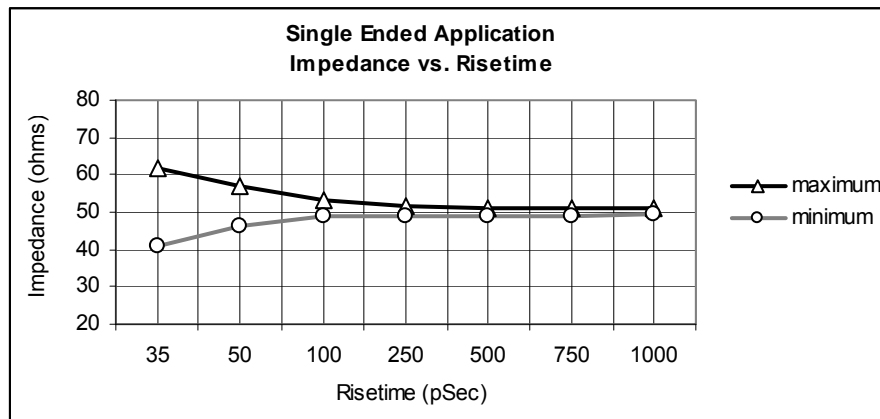
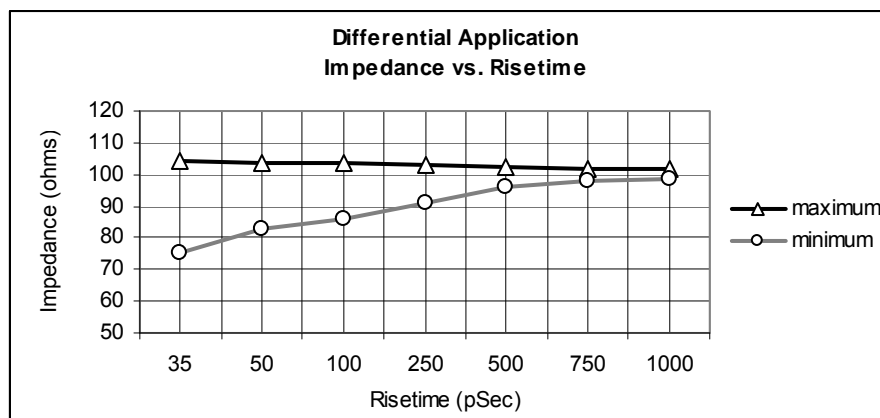


Table 4 - Differential Impedance (Ω)							
Signal Risetime	35±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	104.1	103.7	103.5	102.8	102.3	101.9	101.8
Minimum Impedance	75.2	83.0	86.0	91.2	96.0	97.8	98.6



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Table 5 - Single-Ended Crosstalk (%)										
Input (t _r)	Source	Victim	35±5ps	50ps	100ps	250ps	500ps	750ps	1ns	
NEXT	μp1 to μp3	AW_42	AW_40	16.4	15.6	13.2	7.5	4.2	2.9	2.2
		AW_40	AW_44	2.4	1.9	1.5	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		AW_7	AW_8	4.4	3.9	3.1	1.5	< 1.0%	< 1.0%	< 1.0%
FEXT	μp1 to μp5	AW_42	CLE_40	7.8	6.4	5.2	2.6	1.4	< 1.0%	< 1.0%
		AW_40	CLE_44	3.3	2.0	1.2	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		AW_7	CLE_8	2.7	1.7	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Table 6 - Differential Crosstalk (%)										
Input (t _r)	Source	Victim	35±5ps	50ps	100ps	250ps	500ps	750ps	1ns	
NEXT	μp1 ⁺ 2 ⁻ to μp3 ⁺ 4 ⁻	AW_94-92	AW_90-88	5.1	4.7	3.8	2.2	1.3	< 1.0%	< 1.0%
		AW_84-86	AW_90-92	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		AW_6-4	AW_3-5	1.8	1.6	1.2	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	μp1 ⁺ 2 ⁻ to μp5 ⁺ 6 ⁻	AW_94-92	CLE_90-88	1.9	1.3	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		AW_84-86	CLE_90-92	1.1	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		AW_6-4	CLE_3-5	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Table 7 - Propagation Delay			
Configuration		Signal Path	Mated Connector Only
Single-Ended	μp1 to μp8	Tx, port1=AW_44, Rx, port3=CLE_44	54ps
Differential	μp12 to μp78	Tx, port12=AW_90-92, Rx, port34=CLE_90-92	54ps

Pin Map (reference Appendix C for full description of test boards)