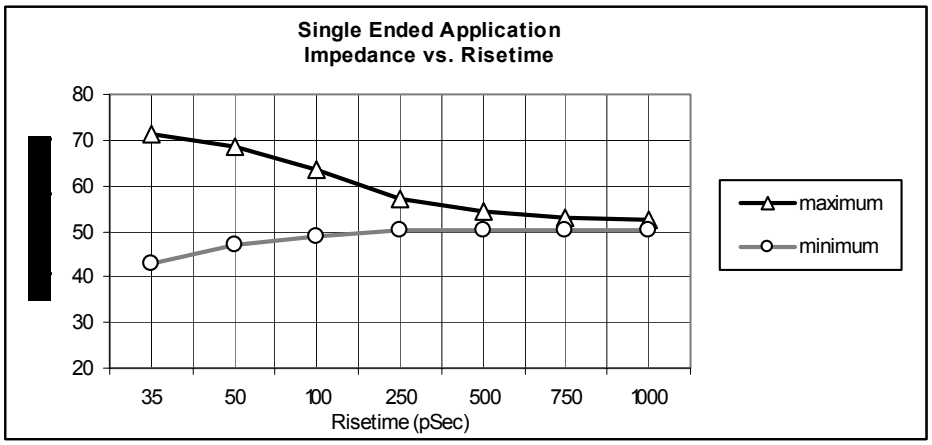
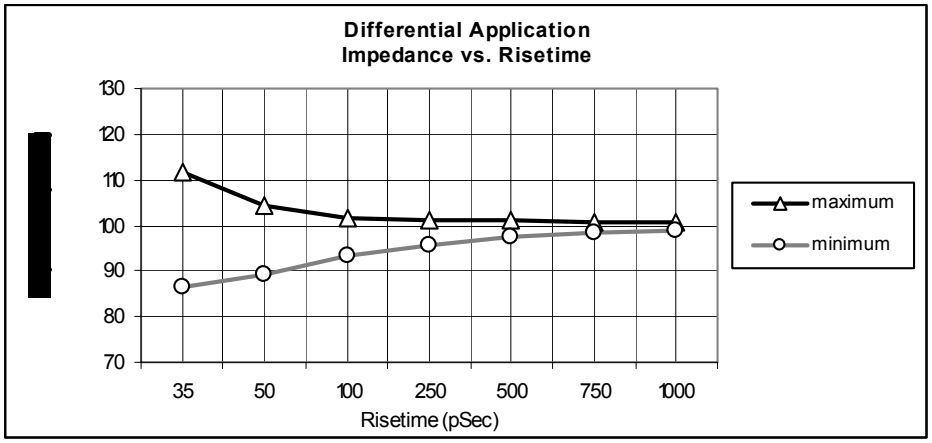
**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

## Time Domain Data Summary

| Table 4 - Single-Ended Impedance (Ω) – Line 132 (SE3) | | | | | | | |
|---|---|---|---|---|---|---|---|
| **Signal Risetime** | **35±5ps** | **50 ps** | **100 ps** | **250 ps** | **500 ps** | **750 ps** | **1 ns** |
| **Maximum Impedance** | 71.2 | 68.5 | 63.4 | 57.1 | 54.2 | 53.1 | 52.6 |
| **Minimum Impedance** | 42.9 | 47.0 | 48.8 | 50.1 | 50.3 | 50.3 | 50.4 |

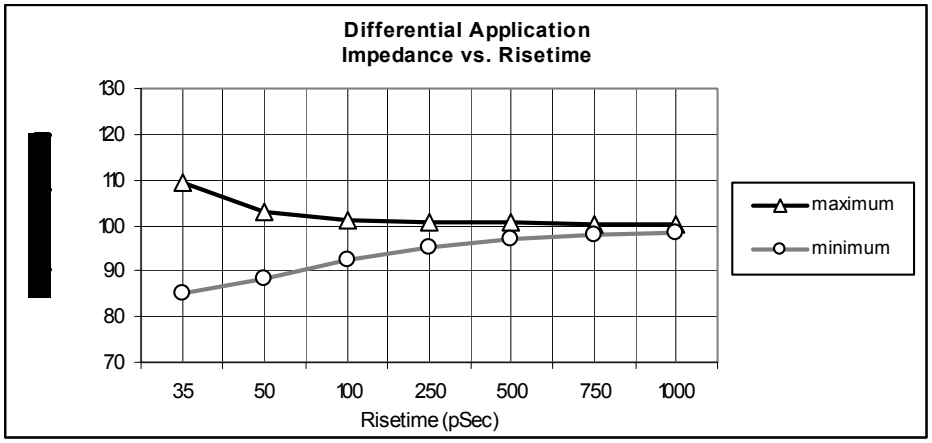



| Table 5 - Optimal Differential Impedance (Ω) – DP2, Pair 95_96 | | | | | | | |
|---|---|---|---|---|---|---|---|
| **Signal Risetime** | **35±5ps** | **50 ps** | **100 ps** | **250 ps** | **500 ps** | **750 ps** | **1 ns** |
| **Maximum Impedance** | 111.8 | 104.4 | 101.5 | 101.2 | 101.1 | 100.9 | 100.8 |
| **Minimum Impedance** | 86.6 | 89.3 | 93.3 | 95.9 | 97.6 | 98.3 | 98.9 |

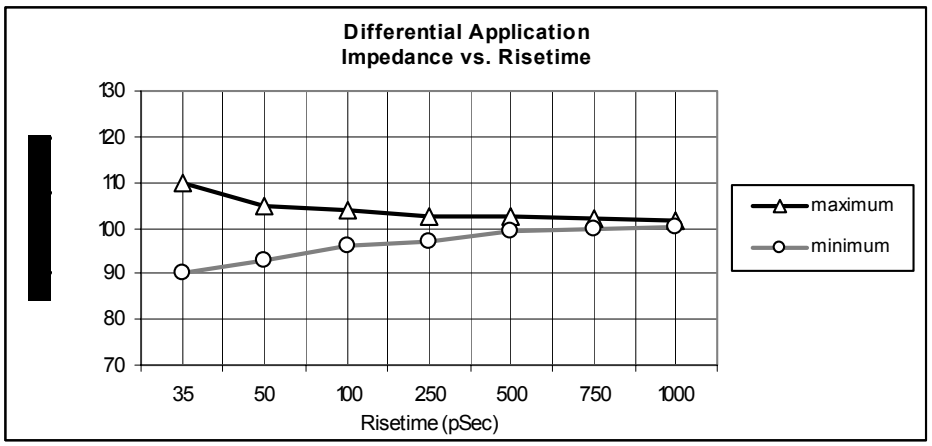


**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

| Table 6 - Optimal Differential Impedance (Ω) – DP3, Pair 49_50 | | | | | | | |
|---|---|---|---|---|---|---|---|
| **Signal Risetime** | **35±5ps** | **50 ps** | **100 ps** | **250 ps** | **500 ps** | **750 ps** | **1 ns** |
| **Maximum Impedance** | 109.4 | 102.9 | 101.2 | 100.8 | 100.7 | 100.3 | 100.1 |
| **Minimum Impedance** | 85.2 | 88.3 | 92.6 | 95.4 | 97.1 | 97.9 | 98.5 |

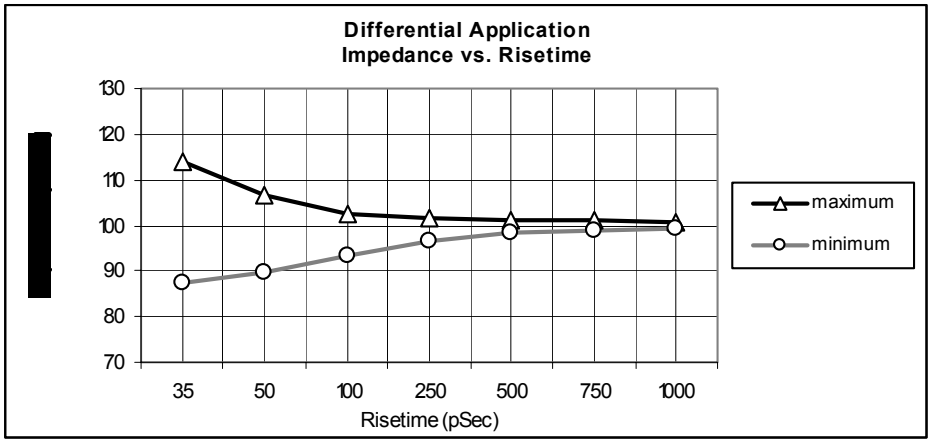



| Table 7 – Standard Differential Impedance (Ω) – DP6, Pair 109_110 | | | | | | | |
|---|---|---|---|---|---|---|---|
| **Signal Risetime** | **35±5ps** | **50 ps** | **100 ps** | **250 ps** | **500 ps** | **750 ps** | **1 ns** |
| **Maximum Impedance** | 109.8 | 104.6 | 103.7 | 102.7 | 102.6 | 101.9 | 101.7 |
| **Minimum Impedance** | 90.0 | 92.8 | 96.0 | 97.3 | 99.2 | 99.9 | 100.1 |



**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

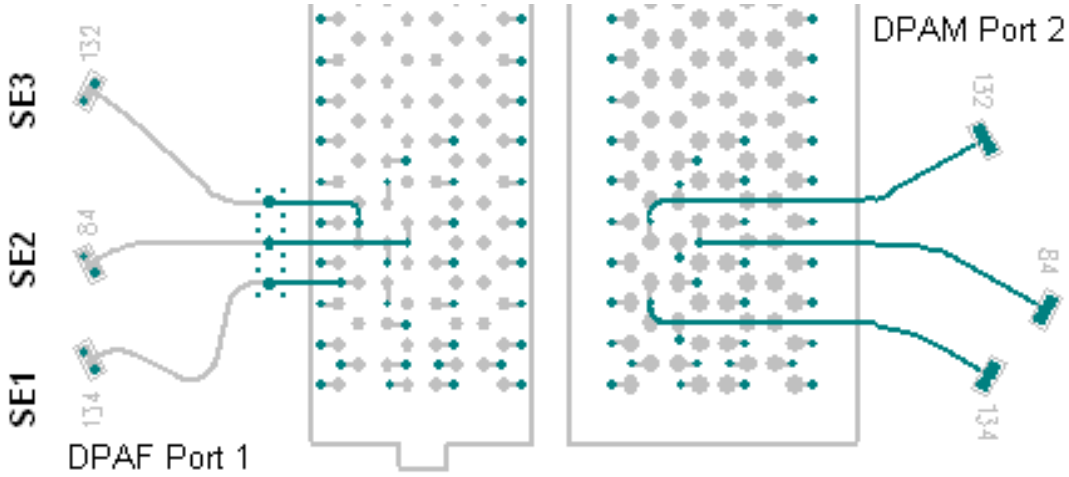
| Table 8- Standard Differential Impedance (Ω) – DP7, Pair 49_50 | | | | | | | |
|---|---|---|---|---|---|---|---|
| **Signal Risetime** | **35±5ps** | **50 ps** | **100 ps** | **250 ps** | **500 ps** | **750 ps** | **1 ns** |
| **Maximum Impedance** | 113.8 | 106.6 | 102.3 | 101.7 | 101.3 | 101.0 | 100.9 |
| **Minimum Impedance** | 87.5 | 89.6 | 93.6 | 96.7 | 98.5 | 99.1 | 99.3 |



**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

| Table 9- Single-Ended Crosstalk (%) | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| **Input ($t_r$)** | **Source** | **Victim** | **30±5ps** | **50ps** | **100ps** | **250ps** | **500ps** | **750ps** | **1ns** |
| **NEXT** | DPAF _134 | DPAF _132 | 4.7 | 3.8 | 3.3 | 2.0 | 1.1 | < 1.0% | < 1.0% |
| | DPAF _84 | DPAF _132 | 3.4 | 1.9 | 1.4 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| **FEXT** | DPAF _134 | DPAM_132 | 4.0 | 2.6 | 1.8 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF _84 | DPAM _132 | 4.7 | 3.1 | 1.8 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |

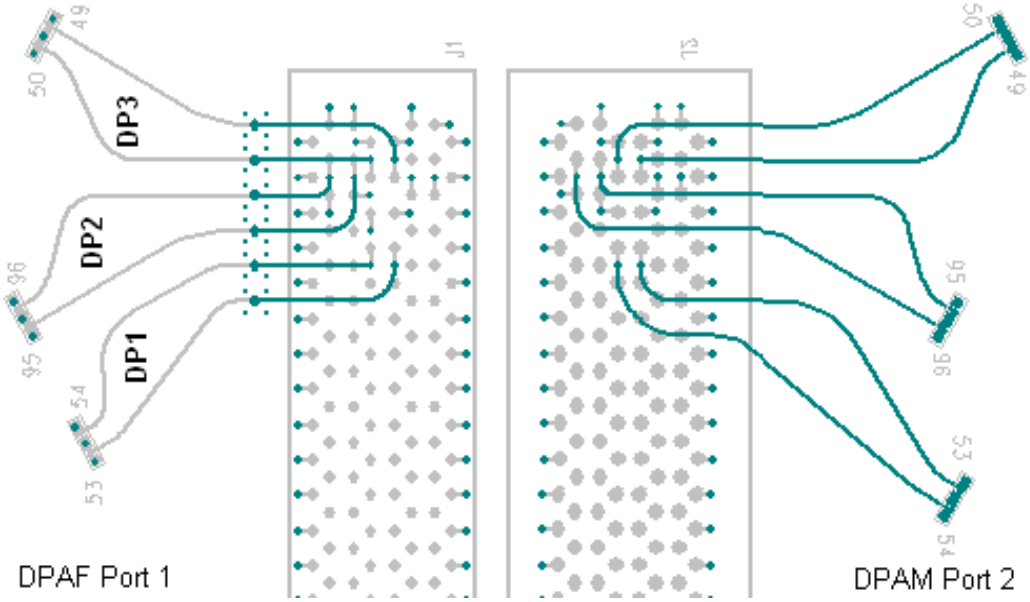
Pin Map (reference Appendix C for full description of test boards)



**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

| Table 10 – Optimal Differential Crosstalk (%) | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| **Input ($t_r$)** | **Source** | **Victim** | **30±5ps** | **50ps** | **100ps** | **250ps** | **500ps** | **750ps** | **1ns** |
| **NEXT** | DPAF_53_54 | DPAF_49_50 | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_95_96 | DPAF_49_50 | 2.3 | 1.9 | 1.6 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| **FEXT** | DPAF_53_54 | DPAM_49_50 | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_95_96 | DPAM_49_50 | 1.6 | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |

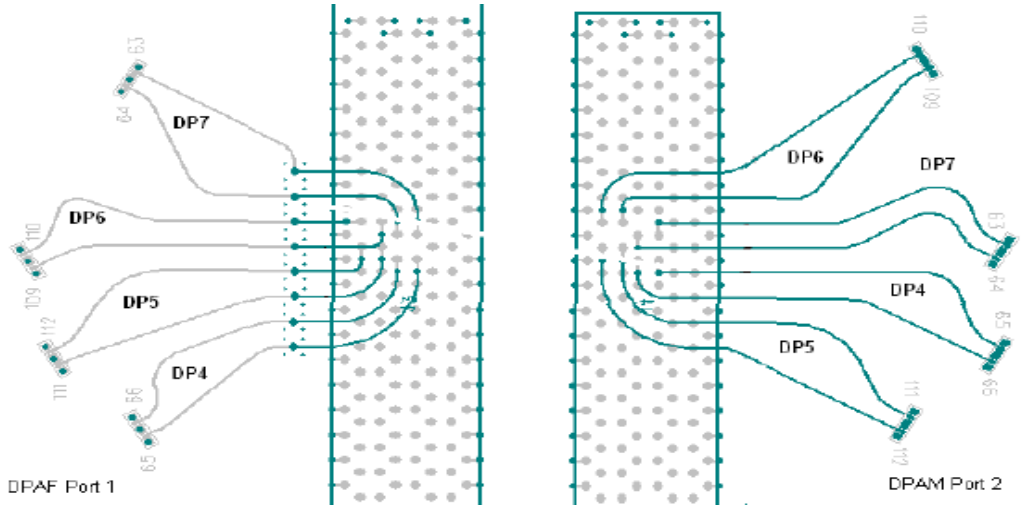
Pin Map (reference Appendix C for full description of test boards)



**Series:** DPAM/DPAF Array, 2.16mm x 2.54mm Differential Pair Interconnect
**Description:** Perimeter Contacts Common to PCB Ground, 10mm Stack Height

| Table 11 – Standard Differential Crosstalk (%) | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| **Input ($t_r$)** | **Source** | **Victim** | **30±5ps** | **50ps** | **100ps** | **250ps** | **500ps** | **750ps** | **1ns** |
| **NEXT** | DPAF_65_66 | DPAF_63_64 | 2.6 | 2.1 | 1.7 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_111_112 | DPAF_63_64 | 1.8 | 1.7 | 1.5 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_109_110 | DPAF_63_64 | 2.4 | 1.8 | 1.4 | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| **FEXT** | DPAF_65_66 | DPAM_63_64 | 1.8 | 1.2 | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_111_112 | DPAM_63_64 | 2.1 | 1.3 | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |
| | DPAF_109_110 | DPAM_63_64 | 1.2 | <1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% | < 1.0% |

Pin Map (reference Appendix C for full description of test boards)




| Table 12 – Propagation Delay | | | | |
|---|---|---|---|---|
| **Configuration** | **Sig. Path** | Mated Connector Only | **Sig. Path** | Mated Connector Only |
| Single-Ended | SE3, 132 | 101ps | | - |
| Optimal Differential | DP2, 95_96 | 99ps | DP2, 49_50 | 97ps |
| Standard Differential | DP6, 109_110 | 97ps | DP7, 63_64 | 96ps |