

Series: FW/CLP Micro Socket and Terminal Board Stacker

Description: Parallel Board-to-Board, 0.050" [1.27mm] Pitch, 17.7mm (0.696") Stack Height

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	97.0	88.8	77.6	61.4	55.4	53.6	52.8
Minimum Impedance	34.8	40.9	46.1	48.2	49.9	51.1	51.5

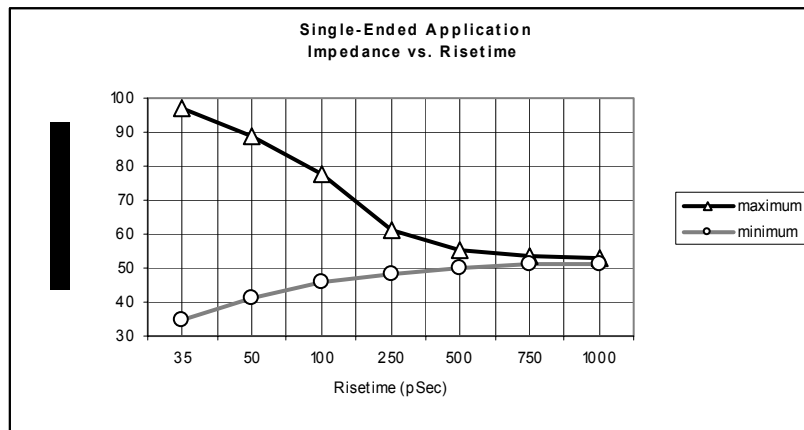
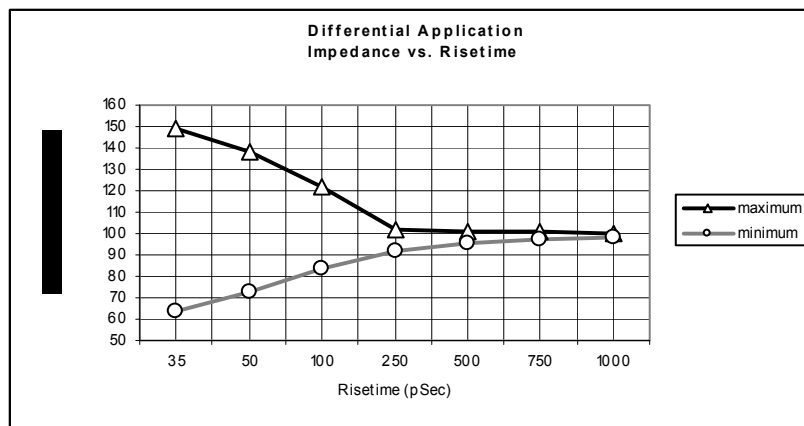


Table 4 - Differential Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	149.1	137.8	121.7	101.5	100.6	100.5	100.4
Minimum Impedance	63.2	73.0	83.8	91.6	95.7	96.9	97.8



Series: FW/CLP Micro Socket and Terminal Board Stacker

Description: Parallel Board-to-Board, 0.050" [1.27mm] Pitch, 17.7mm (0.696") Stack Height

Table 5 - Single-Ended Crosstalk (%)

Input (t _r)		30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	GAQG	14.2	12.4	11.8	8.9	5.4	3.9	3.0
	GAGQG	3.0	2.5	1.8	1.2	< 1.0	< 1.0	< 1.0
	Xrow ^{se}	9.6	8.8	8.3	6.1	3.6	2.6	2.0
FEXT	GAQG	8.3	7.6	6.2	4.0	2.3	1.7	1.3
	GAGQG	3.2	1.9	1.2	< 1.0	< 1.0	< 1.0	< 1.0
	Xrow ^{se}	7.3	5.9	5.3	3.3	1.8	1.3	1.0

Table 6 - Differential Crosstalk (%)

Input (t _r)		30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	GAQG	4.7	4.2	4.1	2.9	1.7	1.2	0.9
	GAGQG	0.5	0.3	0.3	0.2	0.1	0.1	0.1
	Xrow ^{diff}	5.6	5.1	5.0	3.4	1.9	1.3	1.0
FEXT	GAQG	2.0	1.7	1.2	< 1.0	< 1.0	< 1.0	< 1.0
	GAGQG	< 1.0	< 1.0	< 1.0	< 1.0	< 1.0	< 1.0	< 1.0
	Xrow ^{diff}	2.8	2.3	1.8	1.2	< 1.0	< 1.0	< 1.0

Table 7 - Propagation Delay (Mated Connector)

Single-Ended	130ps
Differential	126ps