

Series: LSH-LTH Socket/Terminal Strip

Description: 0.5mm (.0197") Centerline, Low Profile Board-to-Board

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)							
Signal Risetime	35 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	54.2	52.5	52.4	52.0	51.6	51.1	50.9
Minimum Impedance	37.0	39.9	43.1	47.5	49.2	49.2	49.4

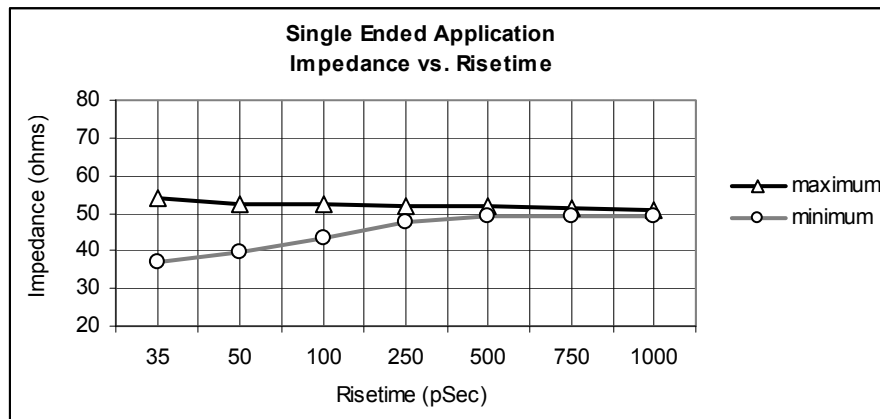
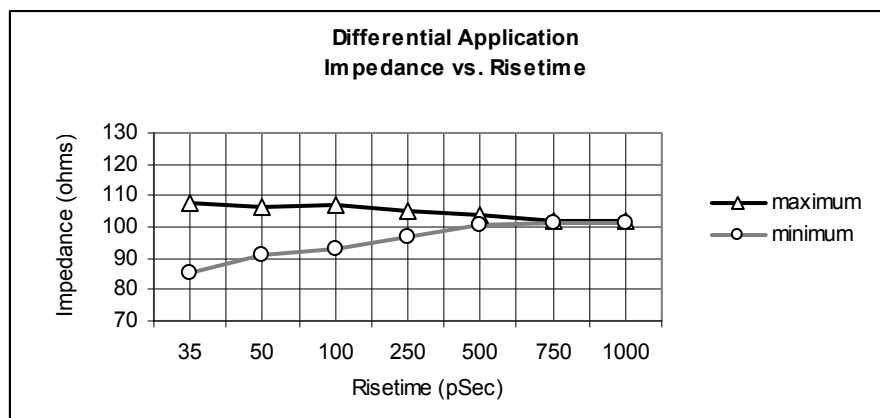


Table 4 - Differential Impedance (Ω)							
Signal Risetime	35 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	107.4	107.1	106.8	105.3	103.4	103.0	102.0
Minimum Impedance	62.7	67.7	77.1	90.7	97.3	99.2	100.0



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Table 5 - Single-Ended Crosstalk (%)

Input (t _r)		Source	Victim	35±5ps	50ps	100ps	250ps	500ps	750ps	1ns
N E X T	μp1 to μp3	LSH_42	LSH_40	14.6	12.4	8.9	4.5	2.5	1.8	1.4
		LSH_44	LSH_40	4.1	2.5	2.0	1.2	< 1.0%	< 1.0%	< 1.0%
		LSH_4	LSH_3	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
F E X T	μp1 to μp5	LSH_42	LTH_40	6.1	4.2	1.5	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		LSH_44	LTH_40	5.9	4.0	2.3	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		LSH_4	LTH_3	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Table 6 - Differential Crosstalk (%)

Input (t _r)		Source	Victim	35±5ps	50ps	100ps	250ps	500ps	750ps	1ns
N E X T	μp1 ⁺ 2 ⁻ to μp3 ⁺ 4 ⁻	LSH_88-90	LSH_84-86	4.1	3.5	2.6	1.6	< 1.0%	< 1.0%	< 1.0%
		LSH_96-98	LSH_90-92	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		LSH_4-6	LSH_3-5	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
F E X T	μp1 ⁺ 2 ⁻ to μp5 ⁺ 6 ⁻	LSH_88-90	LTH_84-86	1.7	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		LSH_96-98	LTH_90-92	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
		LSH_4-6	LTH_3-5	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Table 7 - Propagation Delay

Configuration		Signal Path	Mated Connector Only
Single-Ended	μp1 to μp8	port1=LSH_44 port2=LTH_44	27ps
Differential	μp12 to μp78	port1=LSH_96-98 port2=LTH_96-98	26ps

Pin Map (reference Appendix C for full description of test boards)