

Series: QMSS-DP/QFSS-DP

Description: Shielded Q2 Socket/Terminal Strip, GSSG Differential Pair Shield Configuration, 0.635mm (.025") Pitch, 11mm (.433") Stack Height

Time Domain Data Summary

Table 2 - Differential Impedance (Ω) –pair 93-95							
Signal Risetime	35 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	109.2	106.9	106.0	104.3	103.5	103.3	103.3
Minimum Impedance	61.9	66.2	74.2	89.0	96.1	98.0	98.9

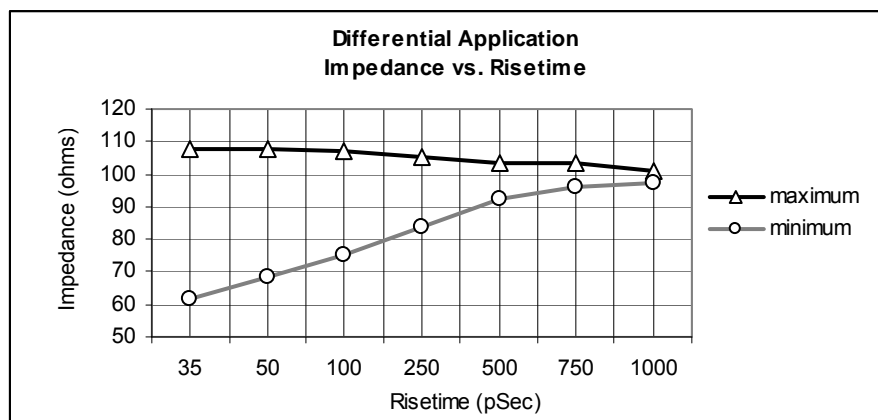


Table 3 - Differential Crosstalk (%)									
Input (t _r)	Source	Victim	35 \pm 5ps	50ps	100ps	250ps	500ps	750ps	1ns
NEXT	QFSS-DP_93-95	QFSS-DP_99-101	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	QFSS-DP_93-95	QFSS-DP_94-96	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	QFSS-DP_93-95	QMSS-DP_99-101	1.1	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	QFSS-DP_93-95	QMSS-DP_94-96	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Table 4 - Propagation Delay			
Configuration		Signal Path	Mated Connector Only
Differential	Ch56 to Ch34	QFSS-DP_93-95, QMSS-DP_93-95	82ps