

Series: TOLC/SOLC, 1.27mm (0.050") Contact Pitch, 0.64mm (0.025") Footprint Pitch
Description:, Board-to-Board, Surface Mount, 6.35mm (0.250") Stack Height

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	61.7	54.4	53.4	51.7	50.8	50.4	50.1
Minimum Impedance	35.2	39.3	43.0	45.3	47.4	48.4	48.8

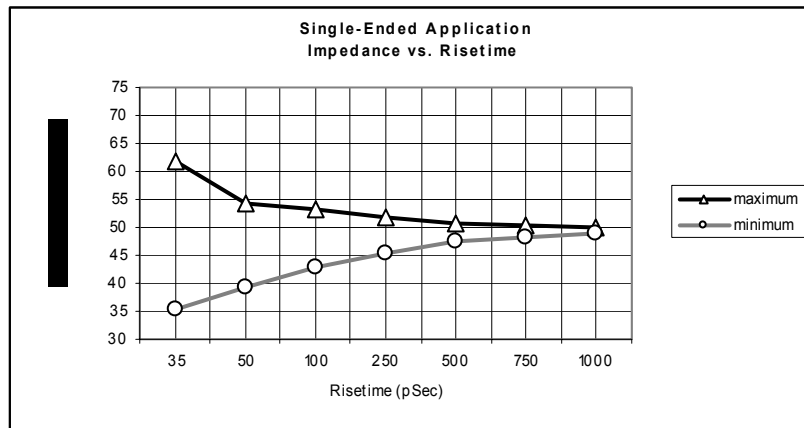
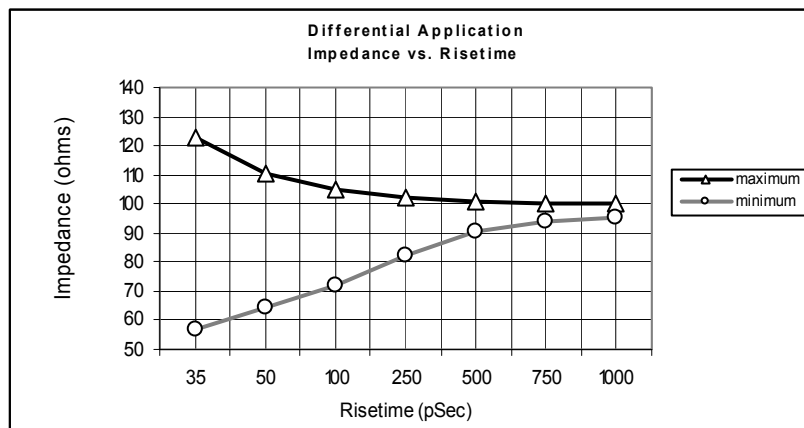


Table 4 - Differential Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	123.0	110.2	105.0	102.1	100.8	100.2	100.1
Minimum Impedance	56.8	64.6	71.7	82.6	90.7	93.9	95.4

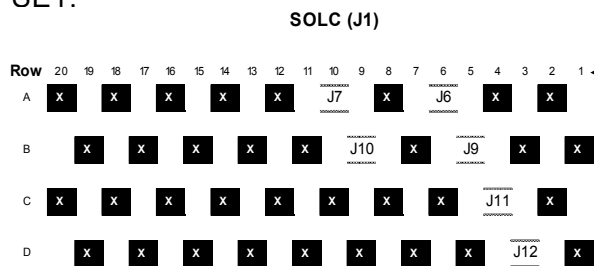


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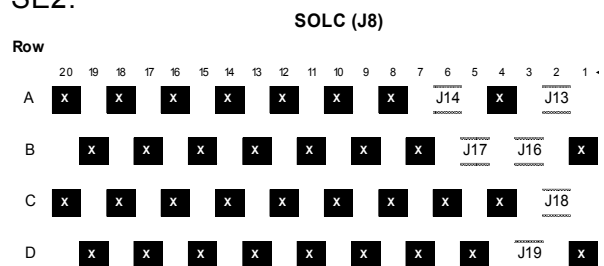
Table 5 - Single-Ended Crosstalk (%)									
Input (t _r)	Source	Victim	30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	J6 (SE1)	J9	10.6	8.3	4.9	2.8	2.8	2.0	1.5
	J9 (SE1)	J11	7.9	6.5	4.8	2.3	1.3	< 1.0%	< 1.0%
	J16 (SE2)	J17	7.5	5.9	4.6	2.6	1.5	1.0	< 1.0%
	J6 (SE1)	J7	2.2	1.7	1.2	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	J6 (SE1)	J37	4.2	1.3	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J9 (SE1)	J39	2.4	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J16 (SE2)	J44	8.8	6.1	3.7	1.4	< 1.0%	< 1.0%	< 1.0%
	J6 (SE1)	J36	2.4	1.7	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Pin Map (reference Appendix C for full description of test boards)

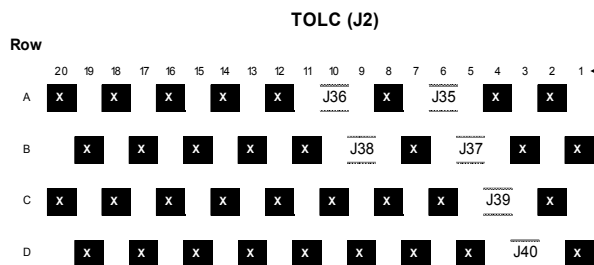
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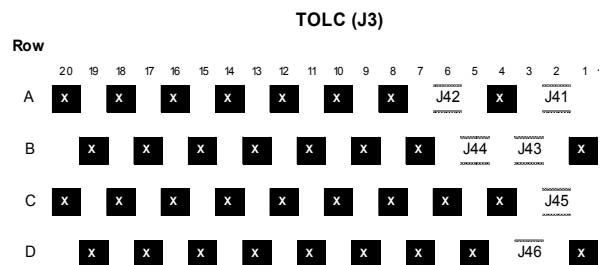
SE2:



Row



Row

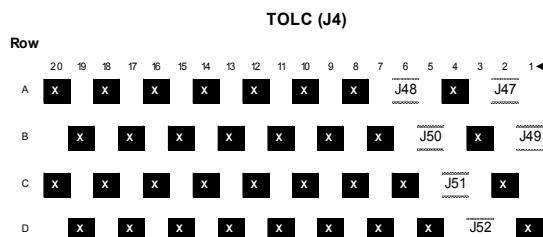
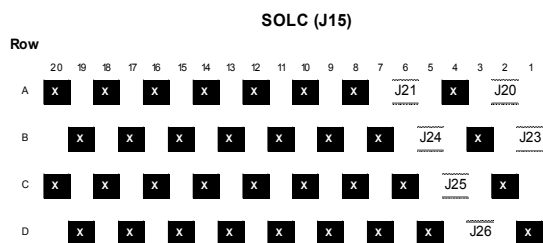


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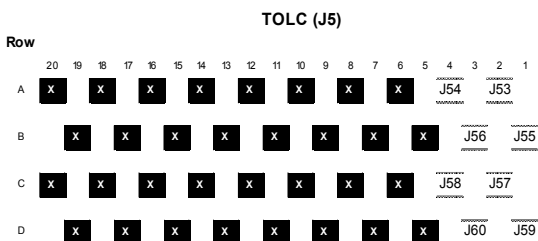
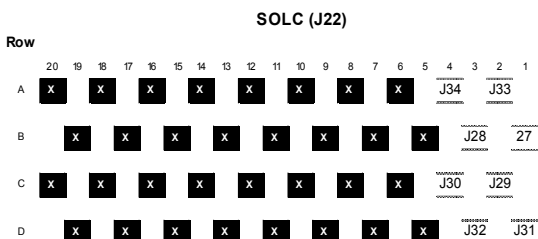
Table 6 - Differential Crosstalk (%)									
Input (t _r)	Source	Victim	30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	J34-J28 (DP2)	J33-J27	4.1	2.9	1.7	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J25-J26	3.4	2.6	1.9	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J14-J17 (SE2)	J13-J16	2.6	2.1	1.6	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J34-J28 (DP2)	J30-J32	3.0	2.1	1.5	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J20-J23	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	J34-J28 (DP2)	J53-J55	6.2	3.8	2.2	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J51-J52	1.7	1.5	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J14-J17 (SE2)	J41-J43	2.1	1.0	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J34-J28 (DP2)	J58-J60	1.8	1.5	1.0	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J47-J49	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Pin Map (reference Appendix C for full description of test boards)

DP1:



DP2:



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Table 7 - Propagation Delay (Mated Connector)	
Single-Ended	54ps
Differential	50ps