

Series: TOLC/SOLC, 1.27mm (0.050") Contact Pitch, 0.64mm (0.025") Footprint Pitch
Description:, Board-to-Board, Surface Mount, 12mm (0.472") Stack Height

Time Domain Data Summary

Table 3 - Single-Ended Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	74.7	66.8	58.8	51.8	50.6	50.4	50.2
Minimum Impedance	35.2	39.3	43.5	47.1	49.2	49.7	49.9

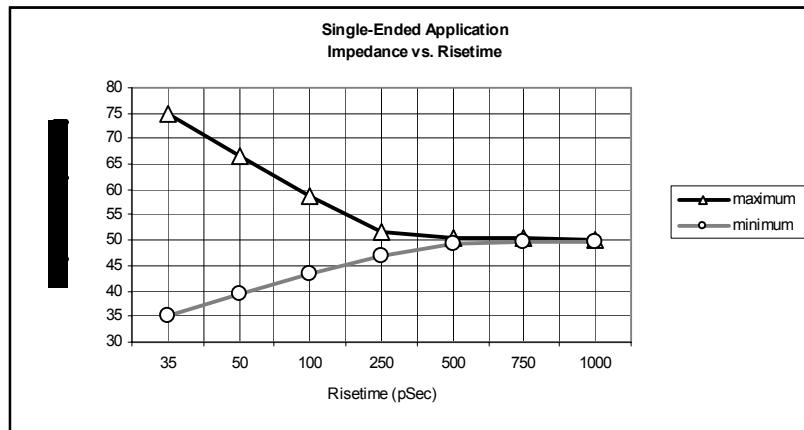
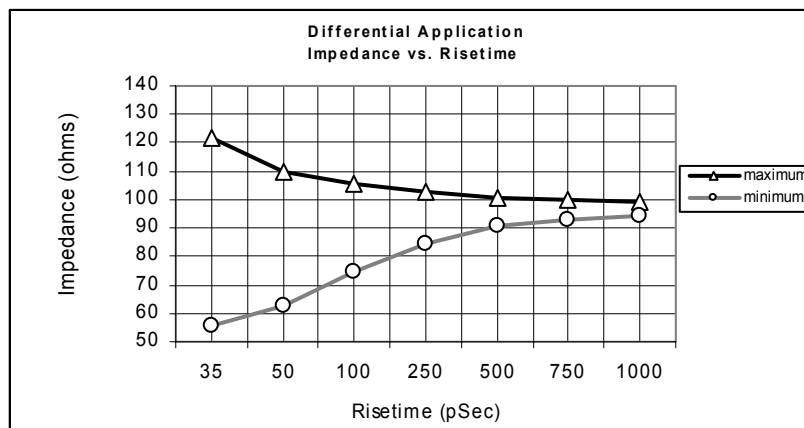


Table 4 - Differential Impedance (Ω)							
Signal Risetime	30 \pm 5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
Maximum Impedance	121.5	110.0	105.8	102.4	100.9	99.7	99.4
Minimum Impedance	55.5	62.5	74.3	84.6	90.4	93.1	94.6



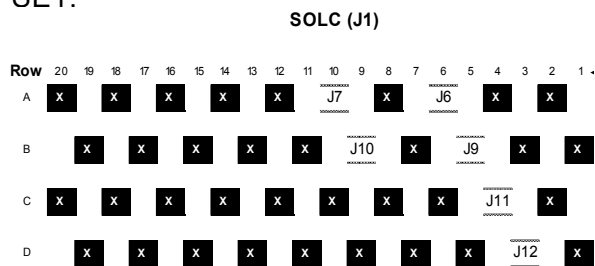
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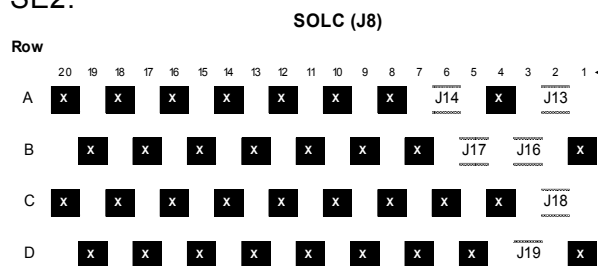
Table 5 - Single-Ended Crosstalk (%)									
Input (t _r)	Source	Victim	30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	J6 (SE1)	J9	11.7	8.1	5.9	3.5	3.5	2.5	1.9
	J9 (SE1)	J11	8.8	7.5	6.2	4.0	2.3	1.6	1.3
	J16 (SE2)	J17	8.1	7.2	6.2	3.6	2.0	1.4	1.1
	J6 (SE1)	J7	2.4	1.9	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	J6 (SE1)	J37	7.9	6.0	4.3	1.9	< 1.0%	< 1.0%	< 1.0%
	J9 (SE1)	J39	3.7	2.5	1.9	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J16 (SE2)	J44	3.4	1.0	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J6 (SE1)	J36	2.2	1.6	1.0	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Pin Map (reference Appendix C for full description of test boards)

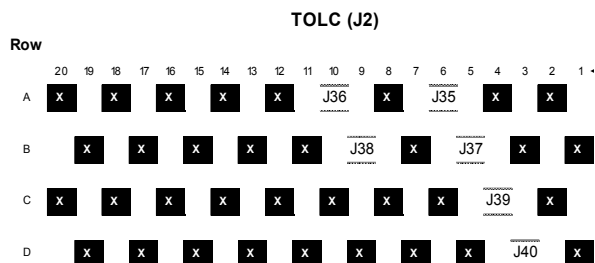
SE1:



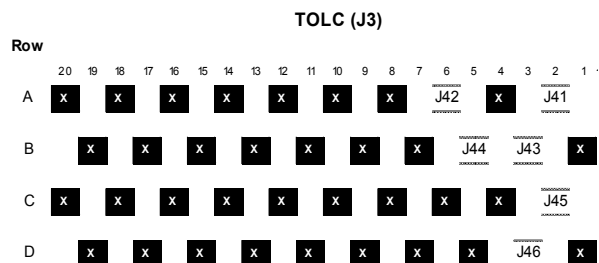
SE2:



Row



Row

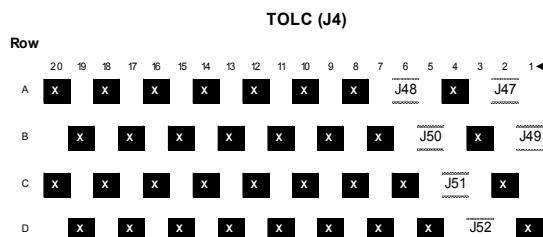
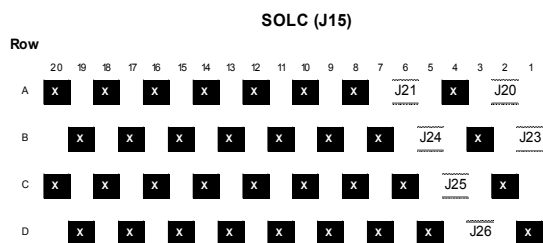


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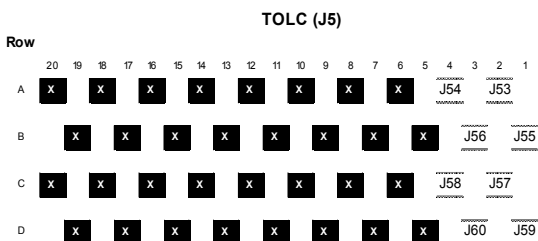
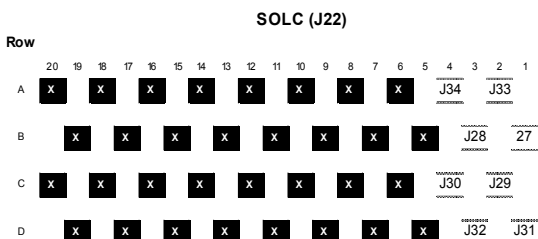
Table 6 - Differential Crosstalk (%)									
Input (t _r)	Source	Victim	30±5ps	50 ps	100 ps	250 ps	500 ps	750 ps	1 ns
NEXT	J34-J28 (DP2)	J33-J27	4.7	3.5	2.9	1.1	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J25-J26	3.6	3.0	2.7	1.5	< 1.0%	< 1.0%	< 1.0%
	J14-J17 (SE2)	J13-J16	3.3	2.8	2.8	1.5	< 1.0%	< 1.0%	< 1.0%
	J34-J28 (DP2)	J30-J32	3.1	2.2	2.1	1.2	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J20-J23	1.0	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
FEXT	J34-J28 (DP2)	J53-J55	6.7	4.6	3.1	1.3	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J51-J52	1.9	1.1	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J14-J17 (SE2)	J41-J43	1.4	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J34-J28 (DP2)	J58-J60	1.3	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%
	J21-J24 (DP1)	J47-J49	1.1	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%	< 1.0%

Pin Map (reference Appendix C for full description of test boards)

DP1:



DP2:



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Table 7 - Propagation Delay (Mated Connector)	
Single-Ended	75ps
Differential	78ps